



Figure 1

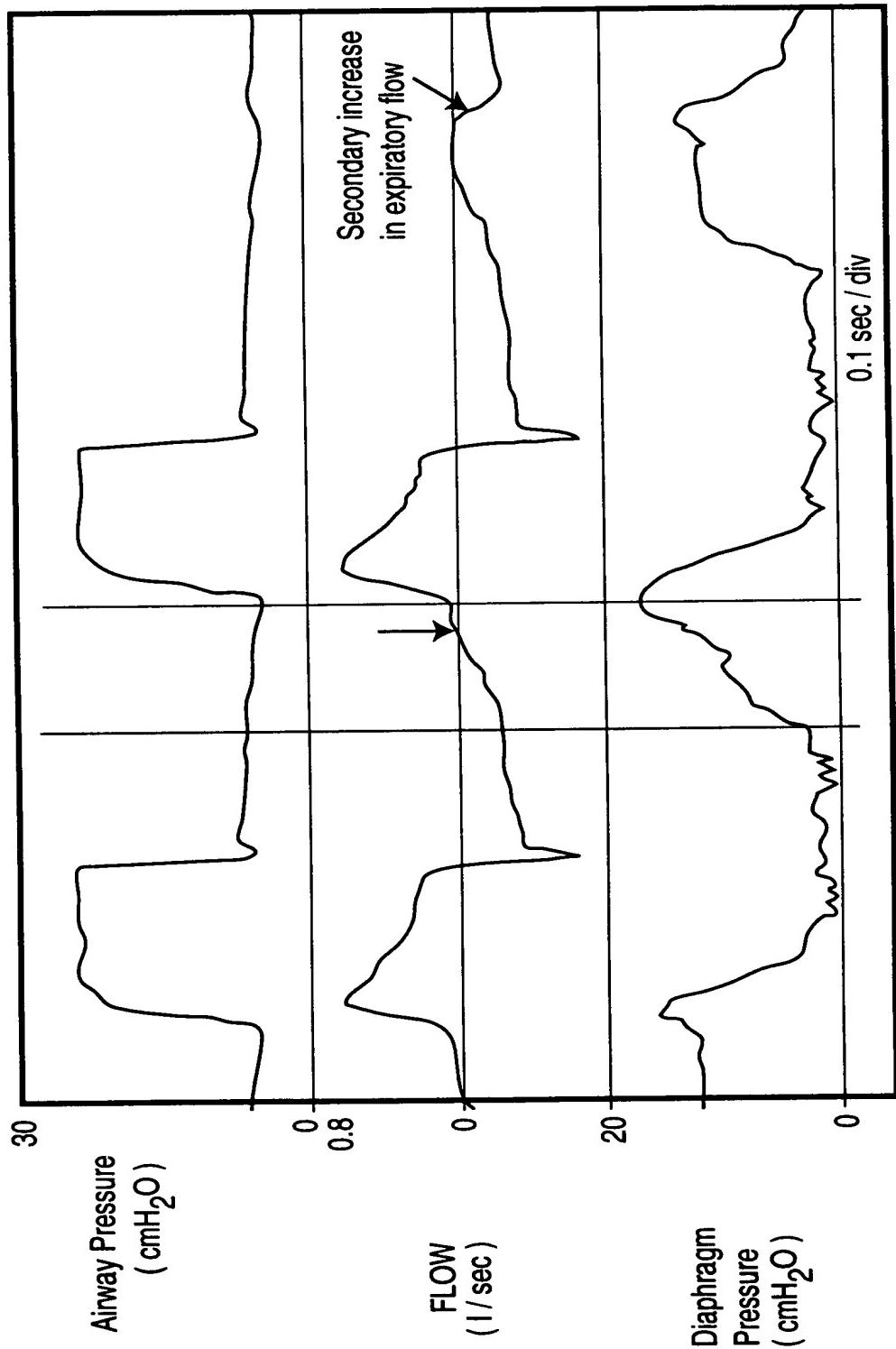




Figure 2

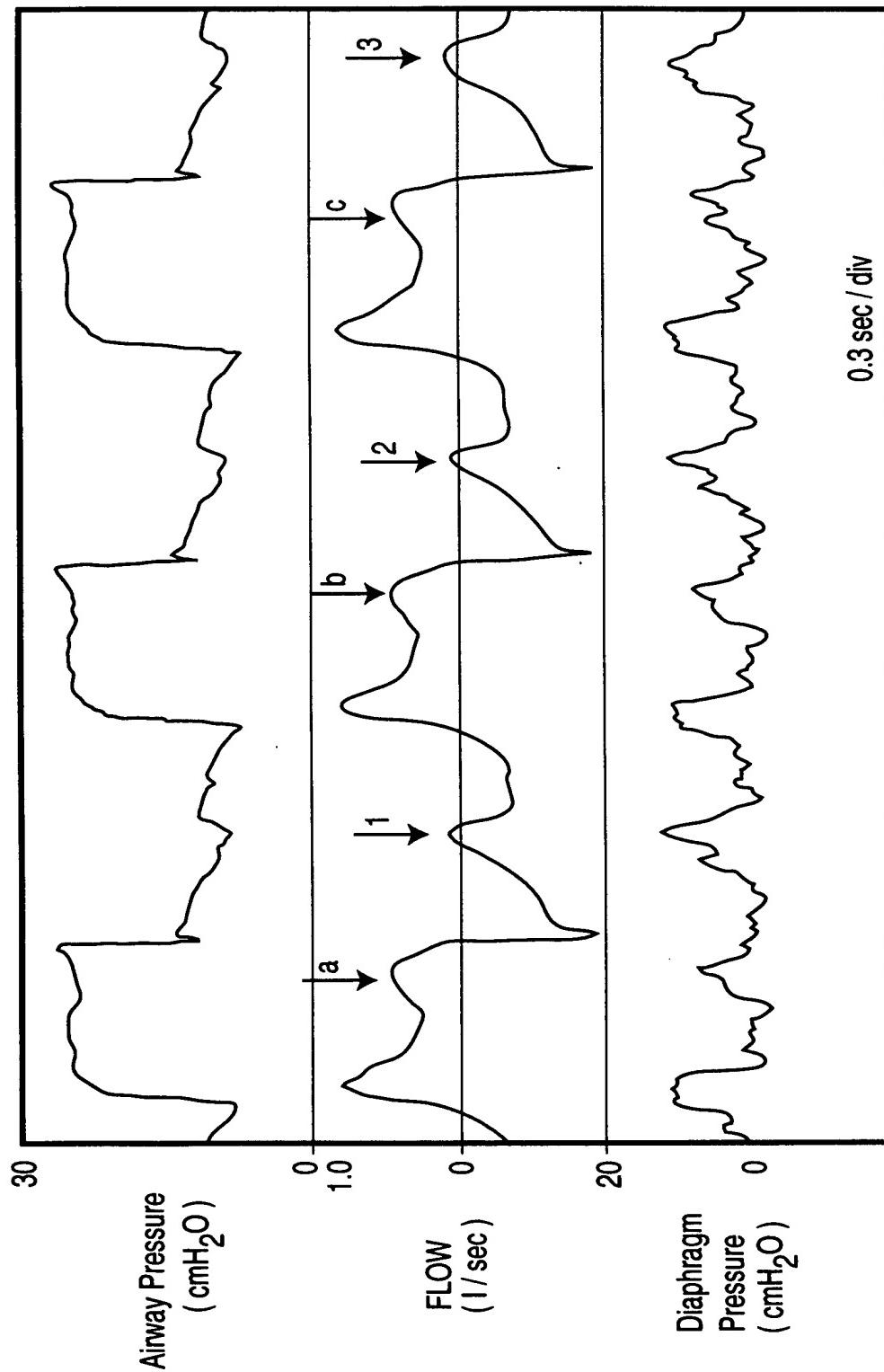




Figure 3

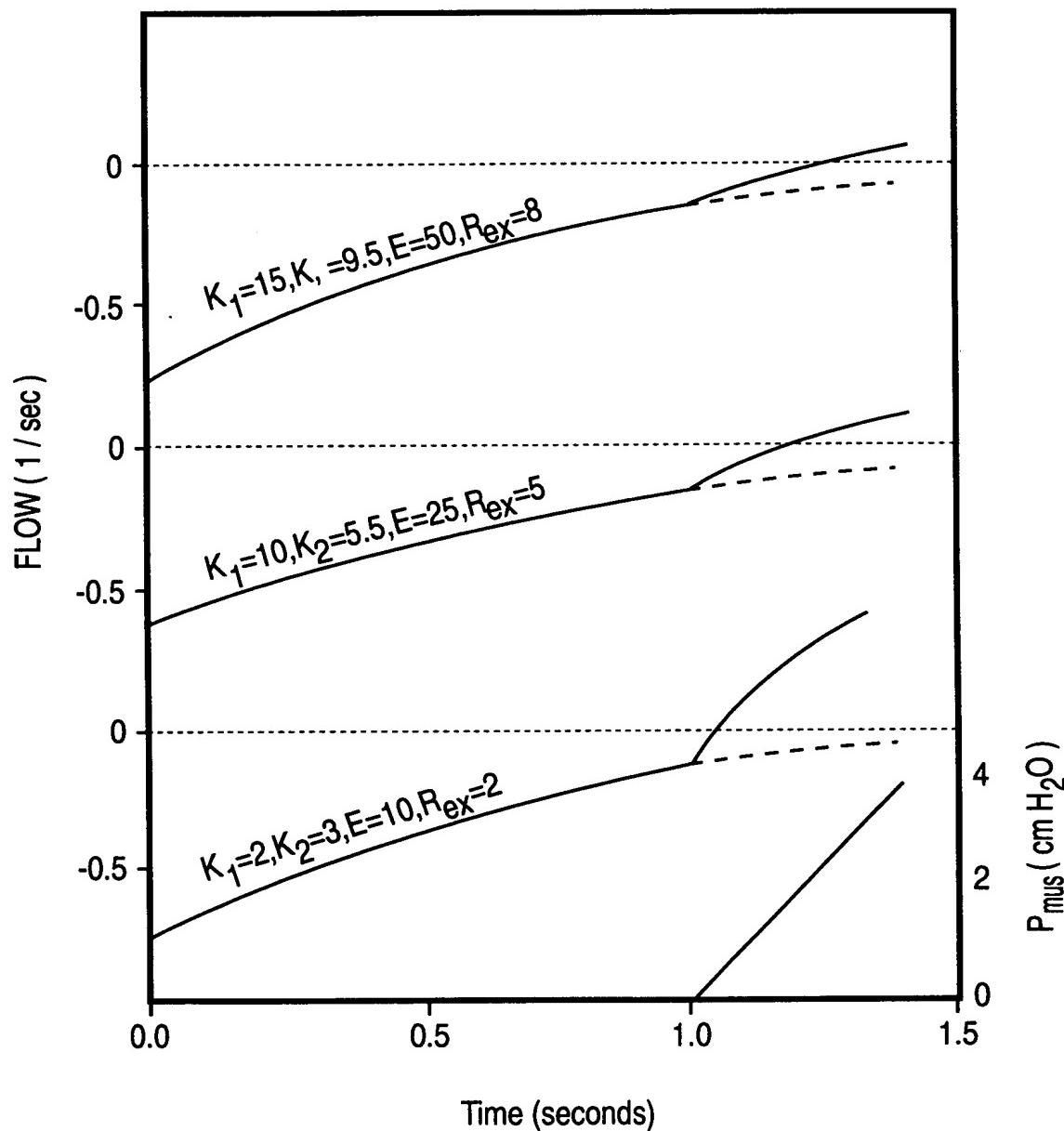




Figure 4

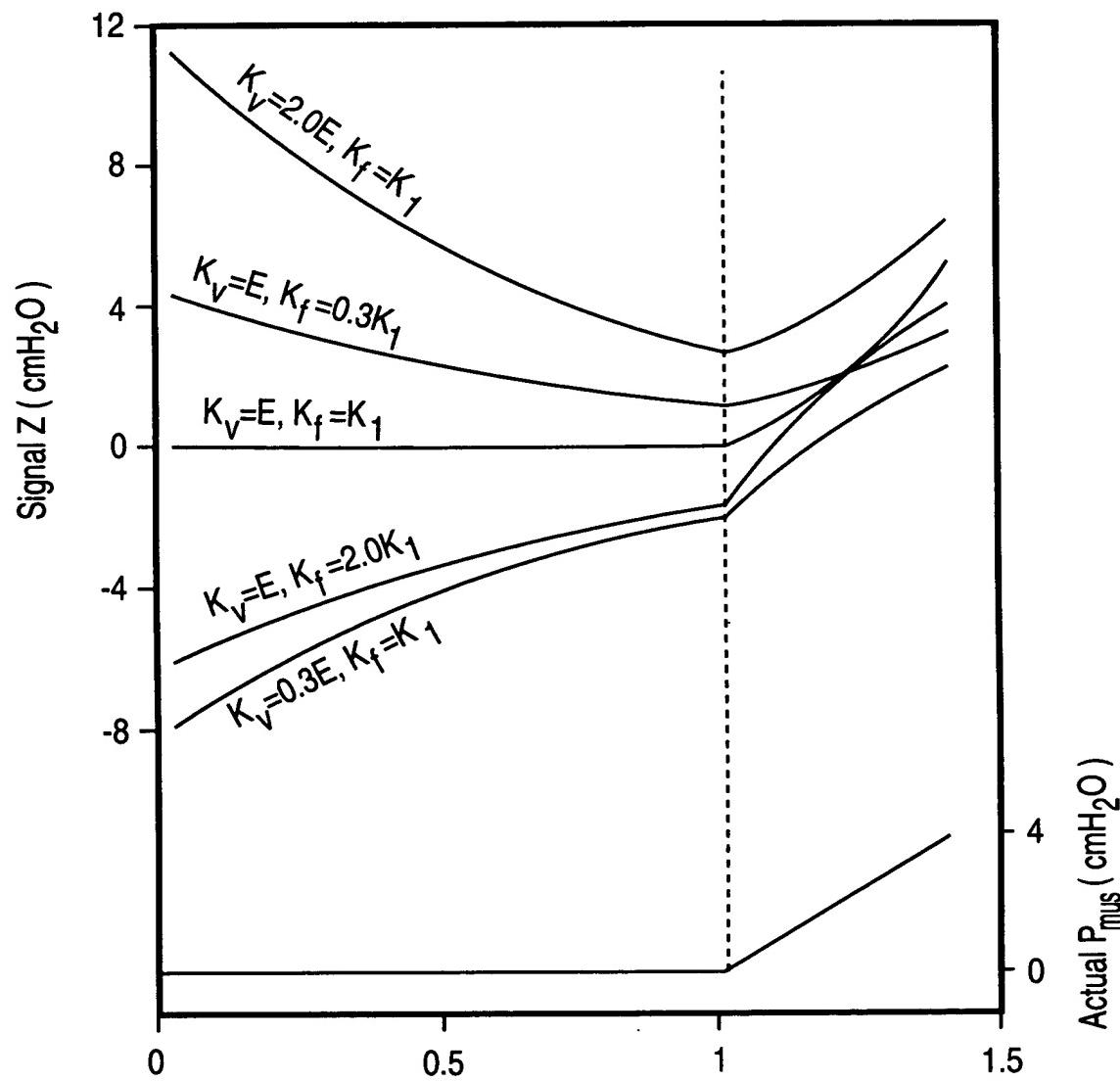




Figure 5

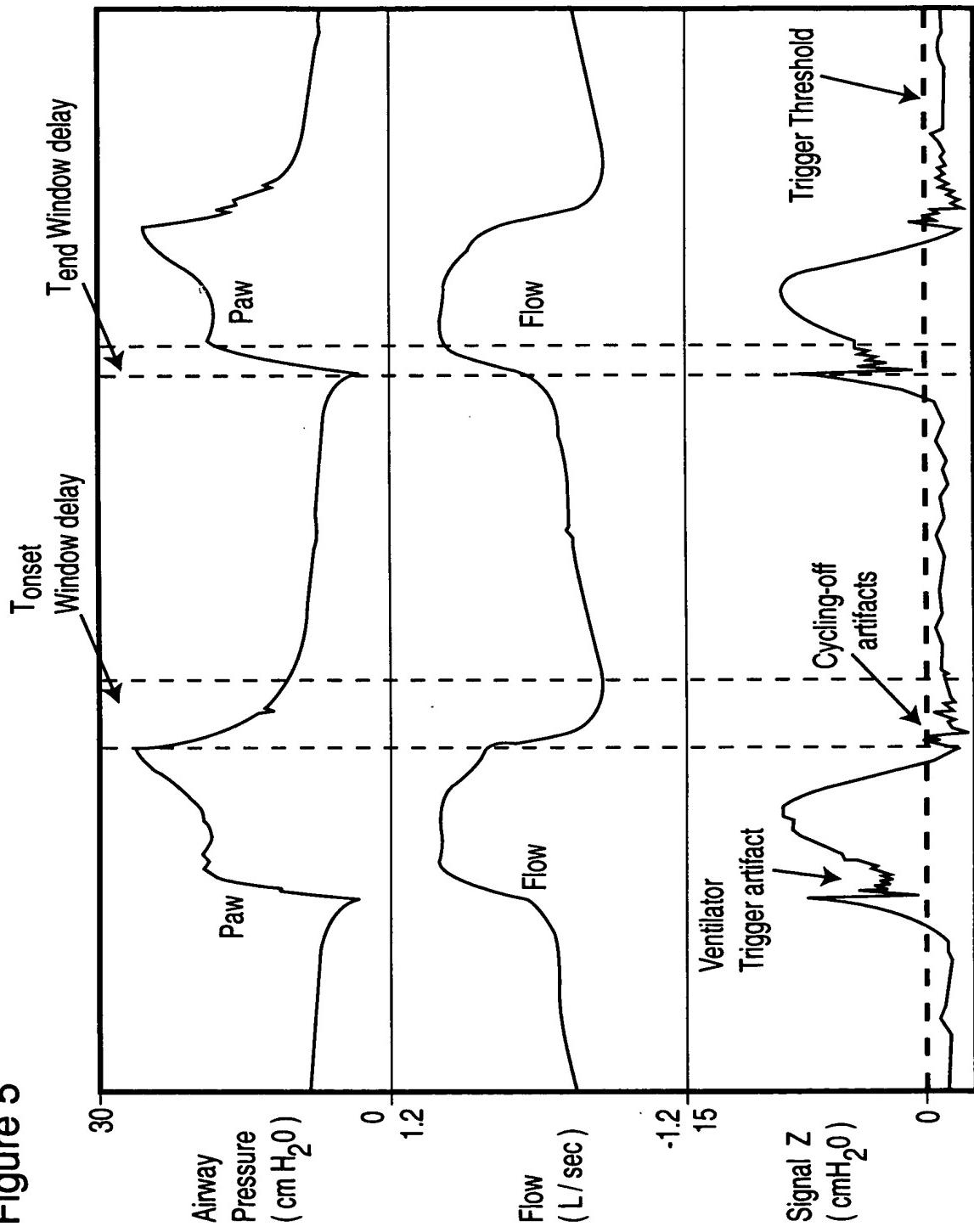




Figure 6

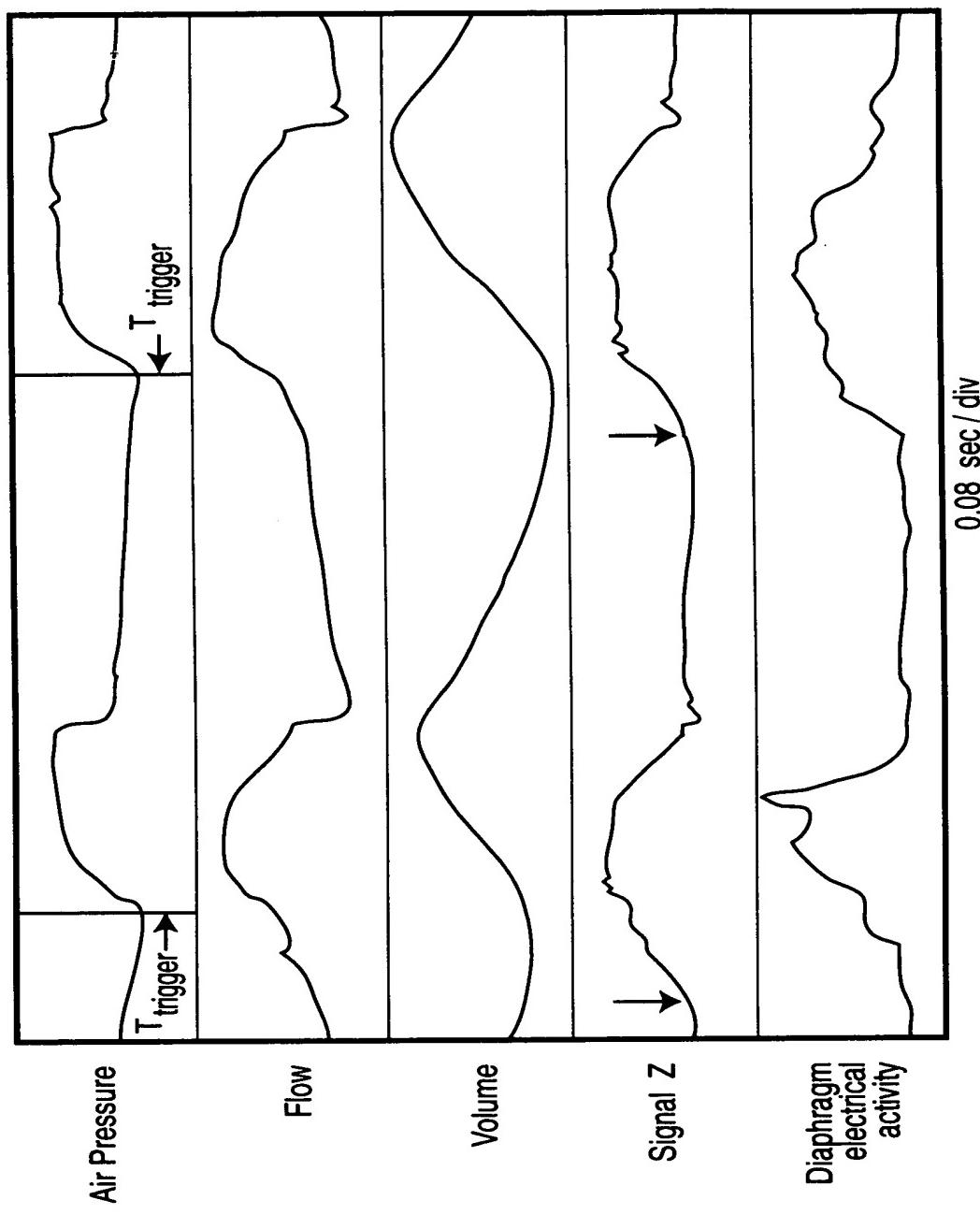




Figure 7

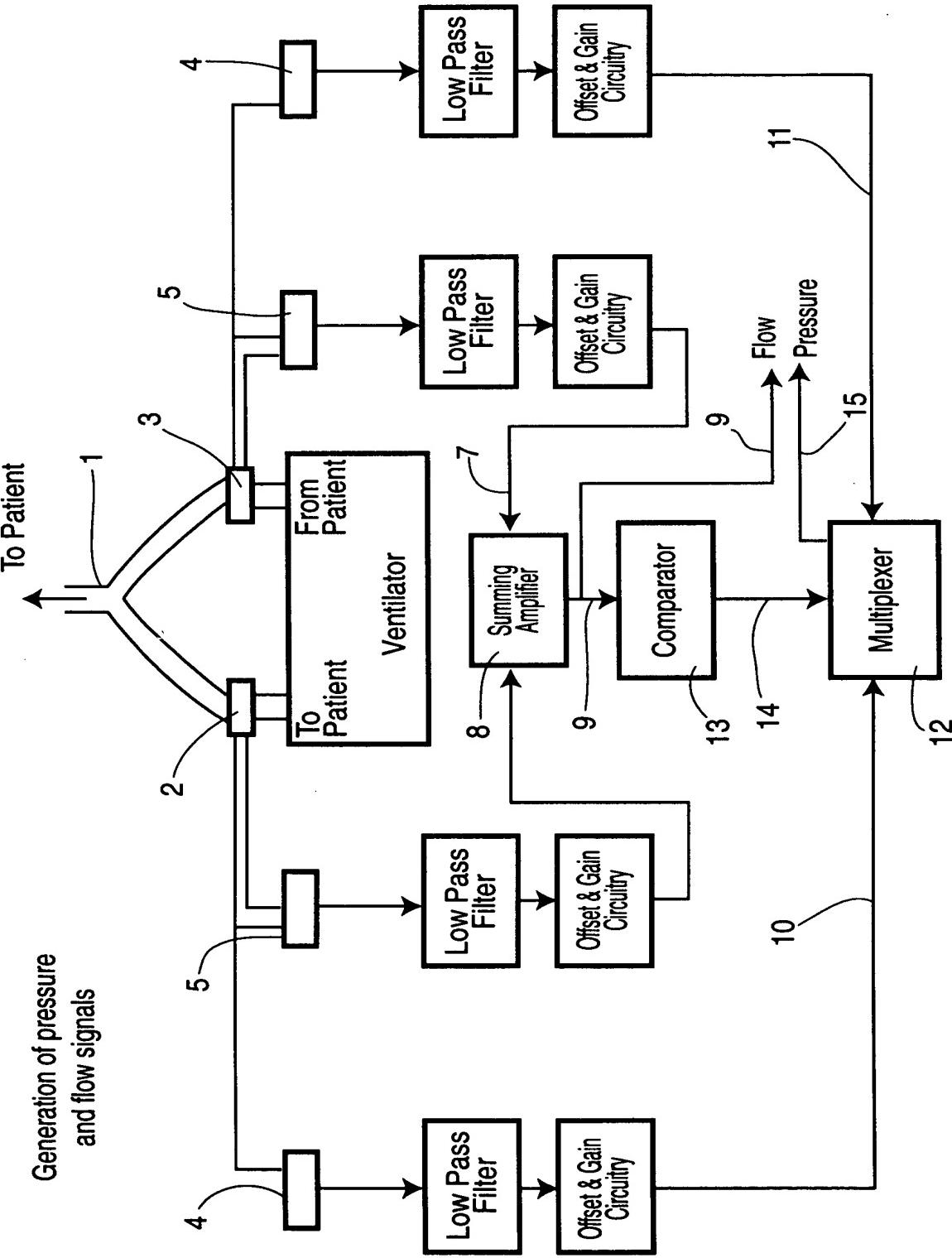




Figure 8A

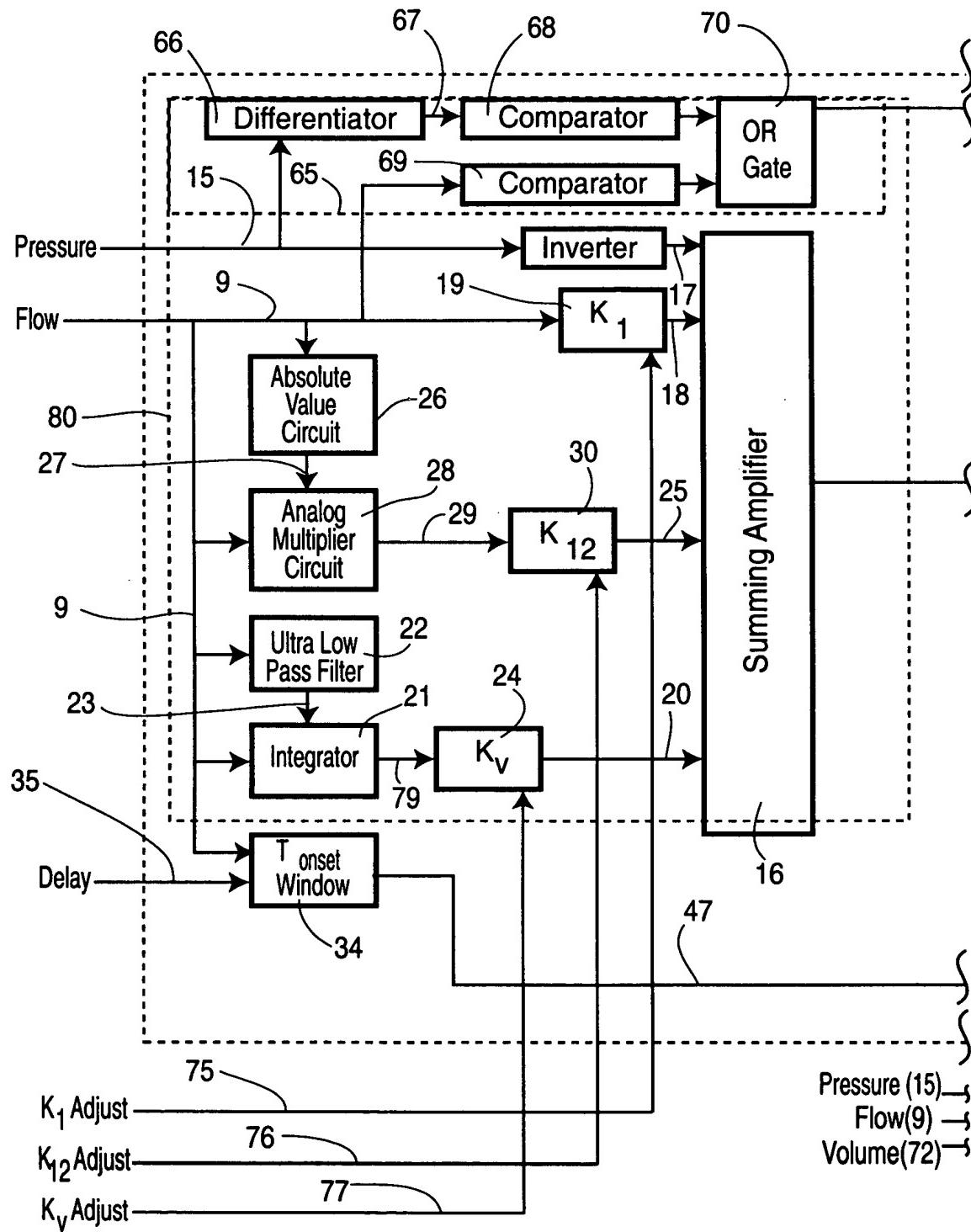




Figure 8B

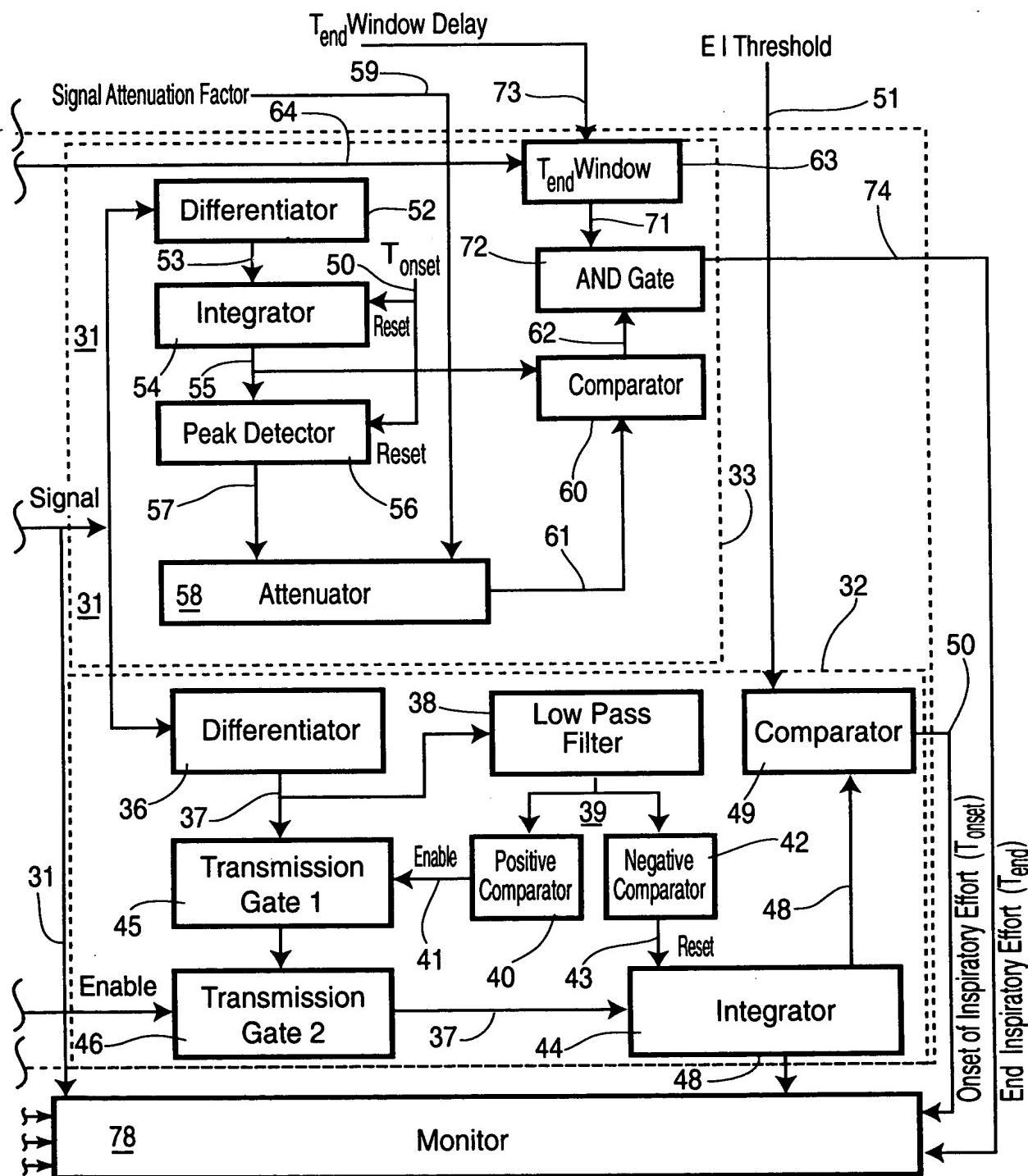




Figure 9

Digital Implementation of Output Functions

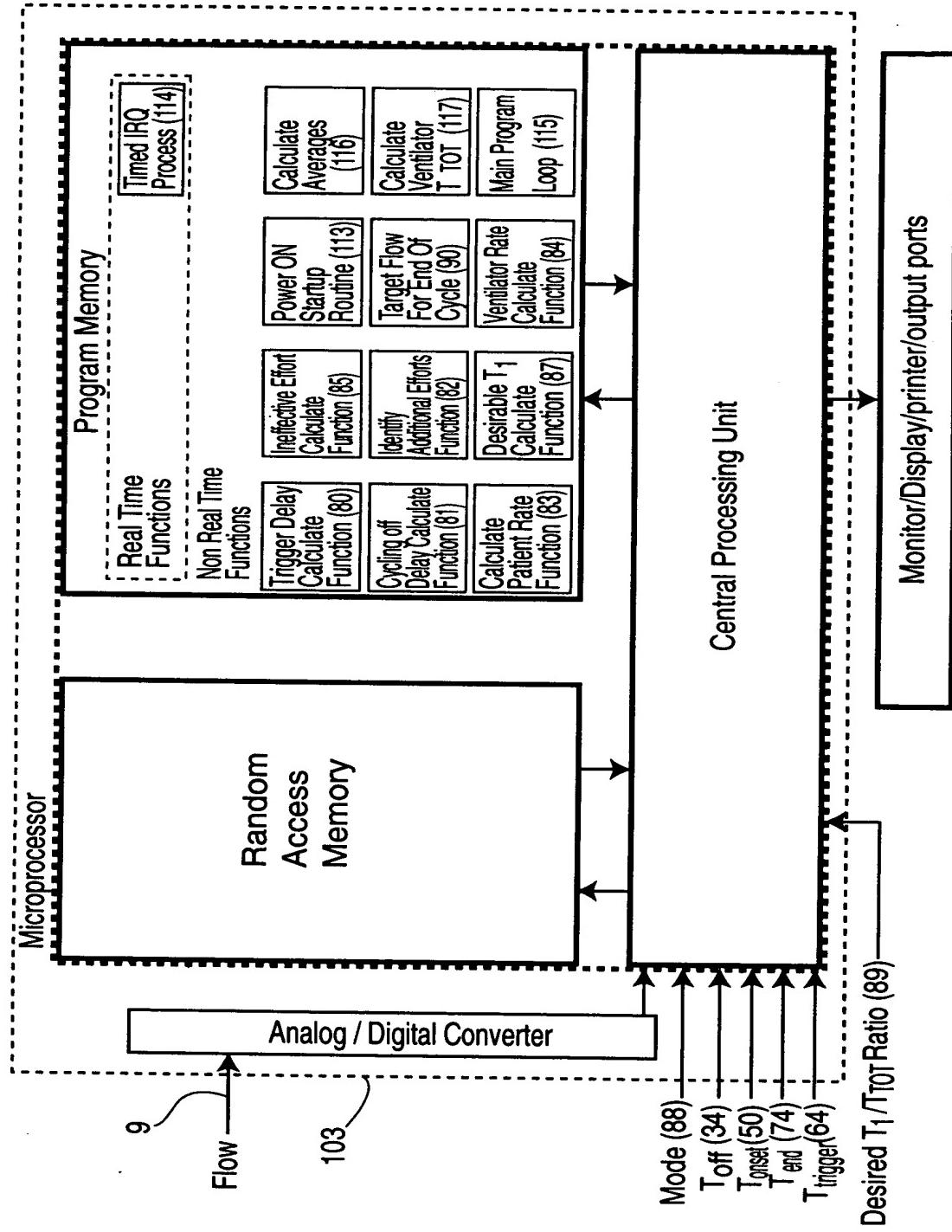




Figure 10

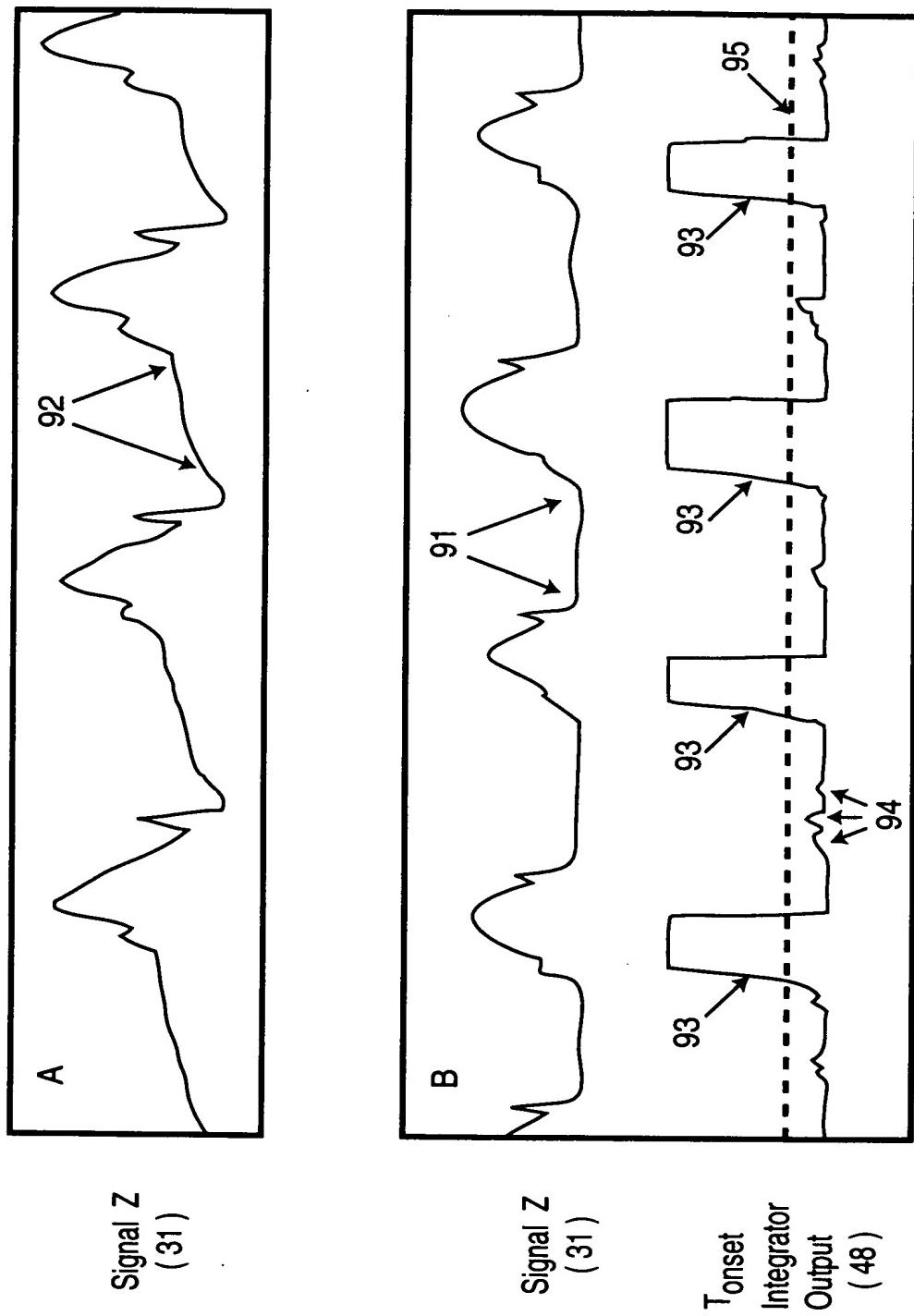




Figure 11B

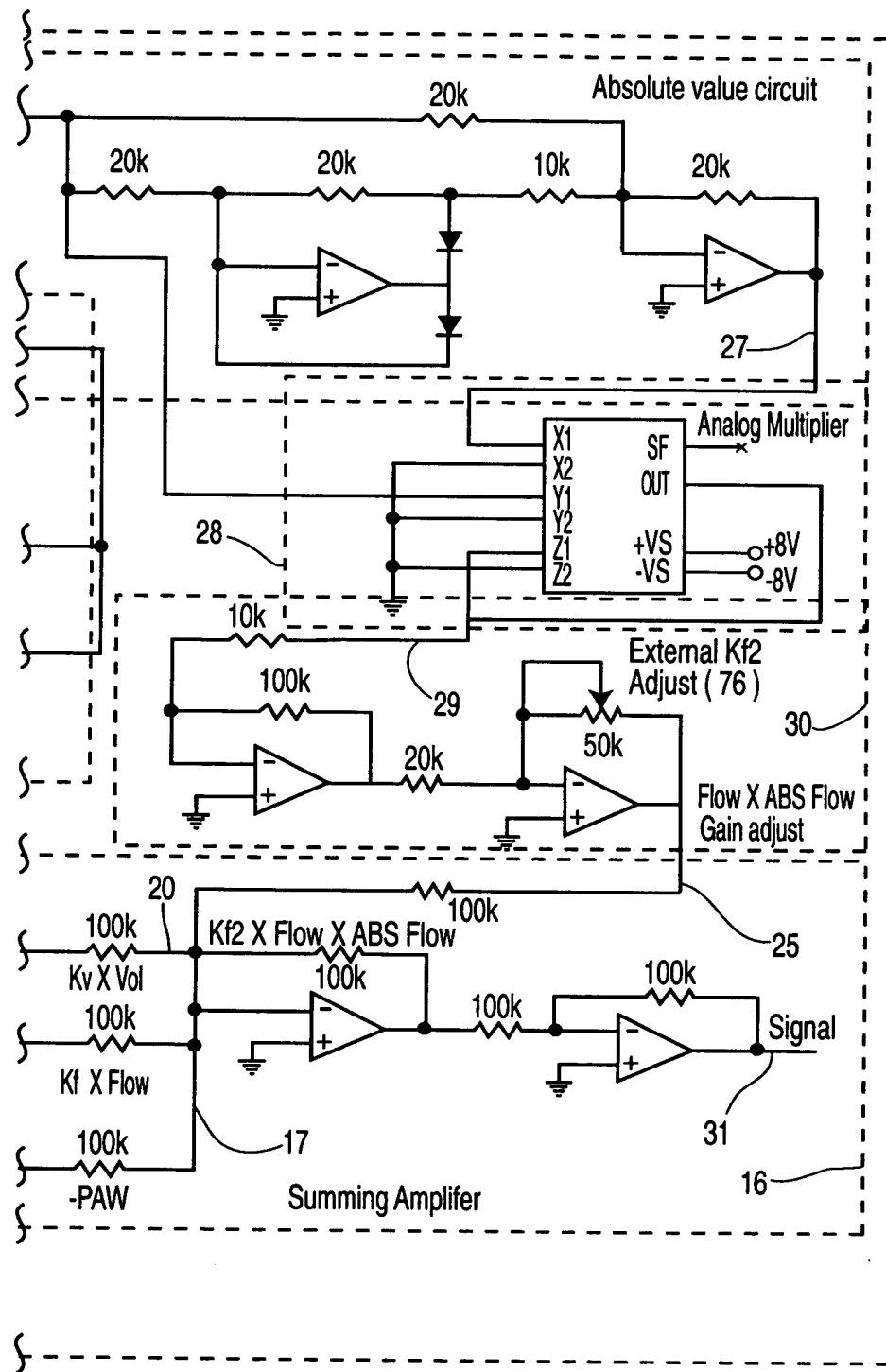




Figure 11A

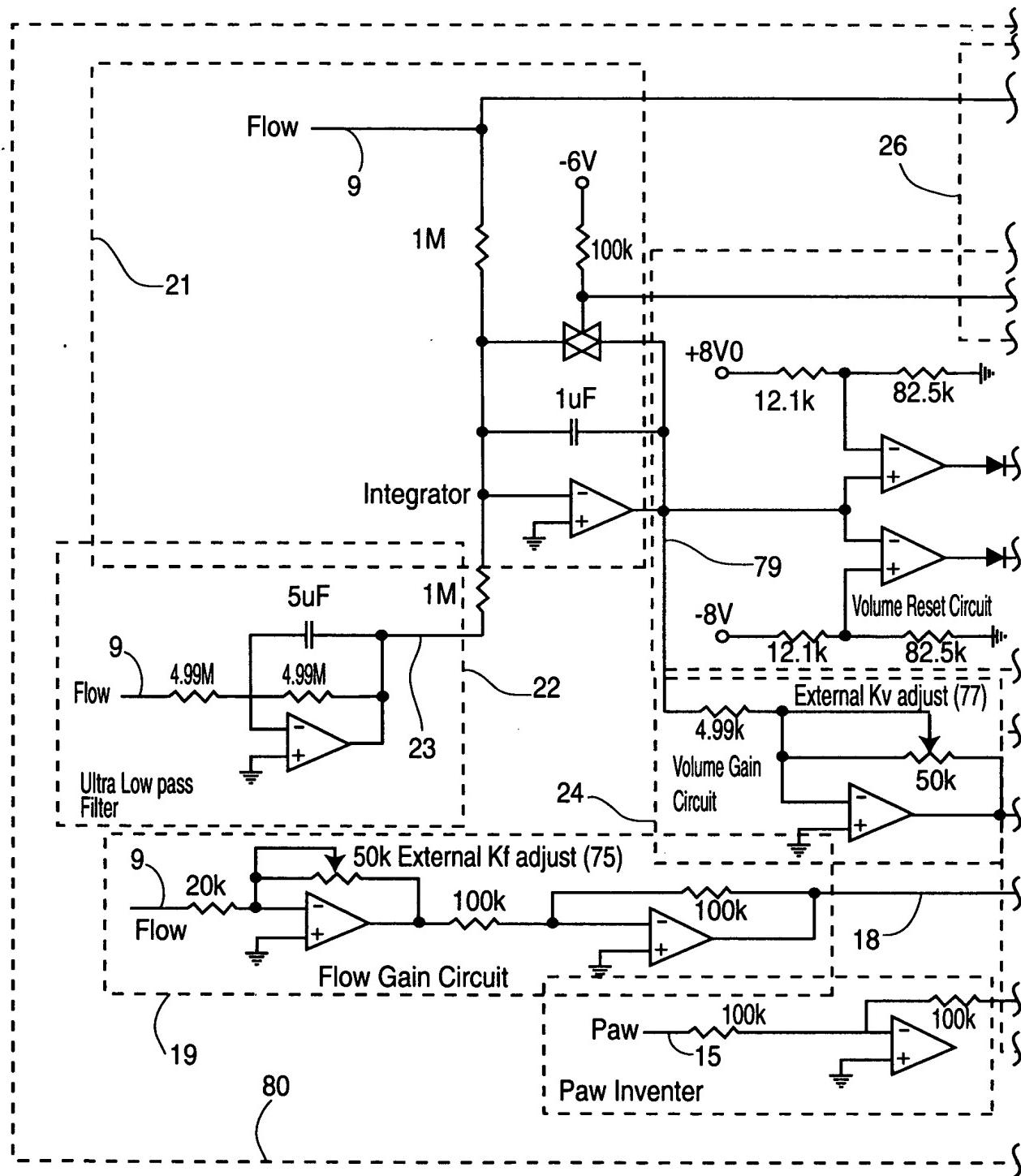




Figure 12 A

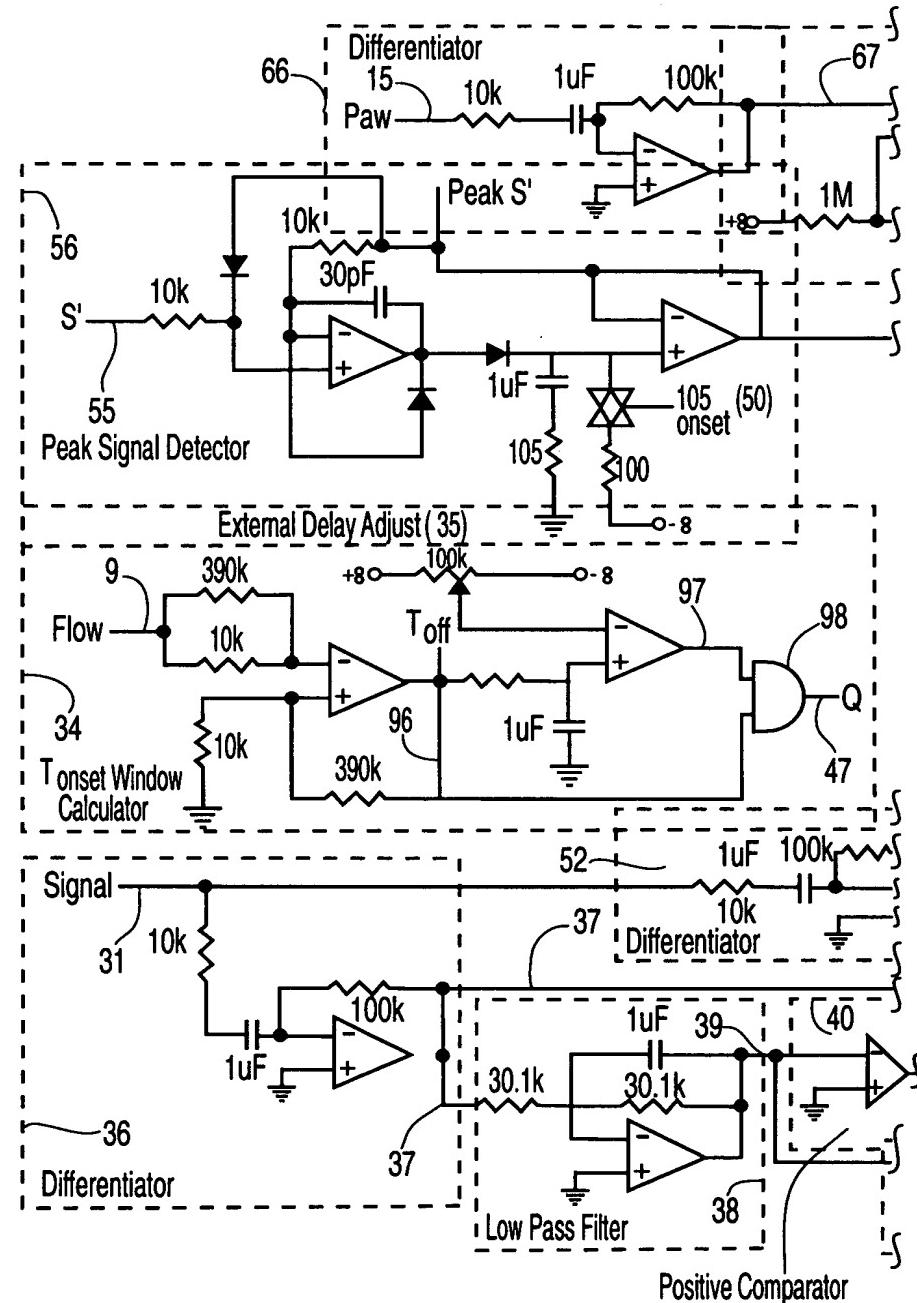




Figure 12 B

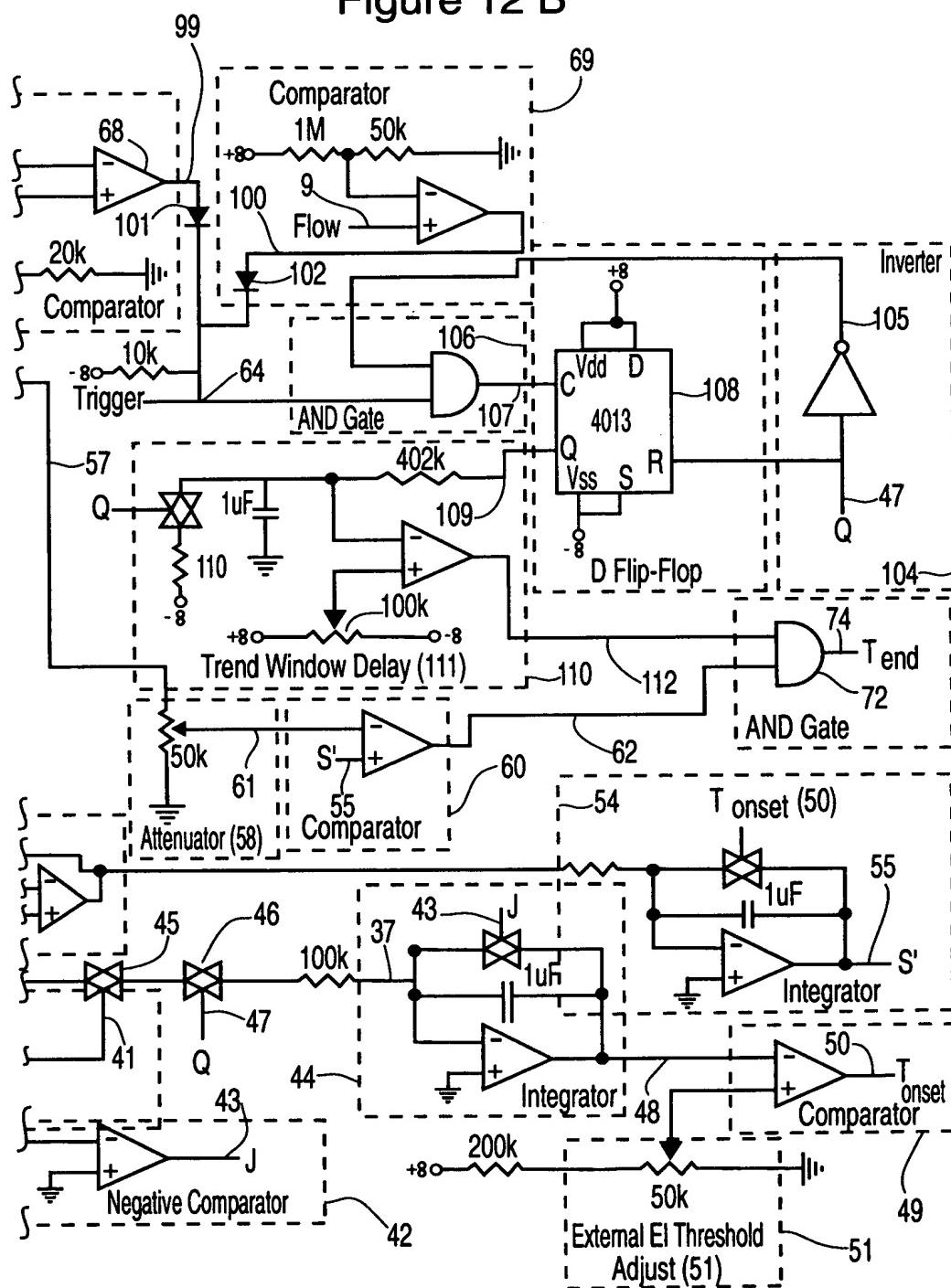




Figure 13

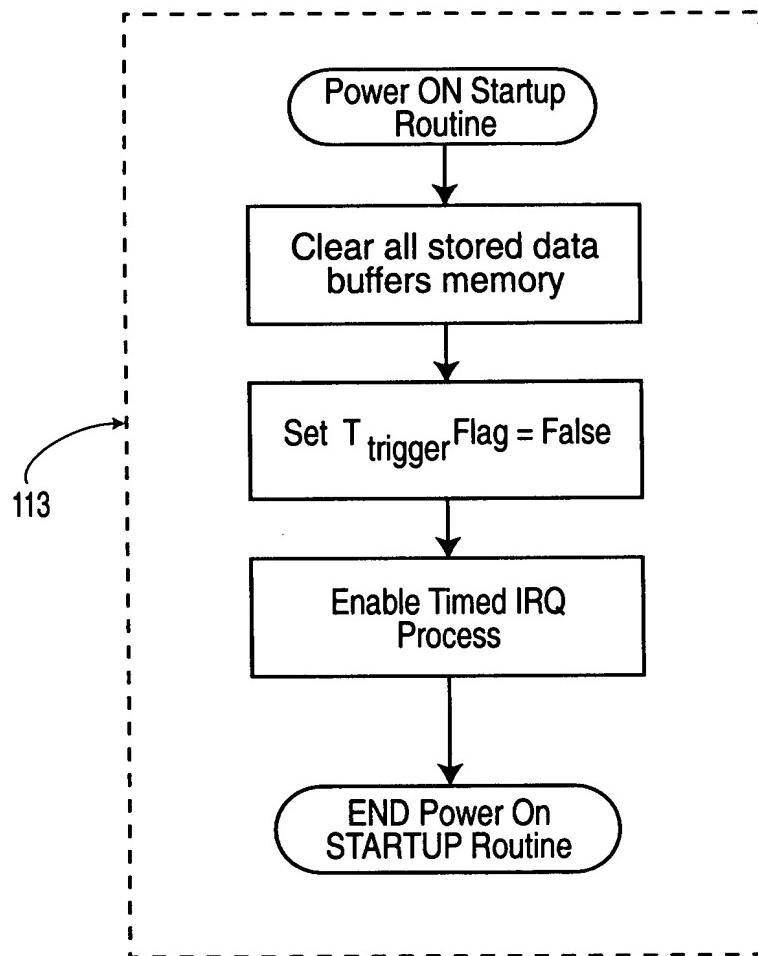




Figure 14 A

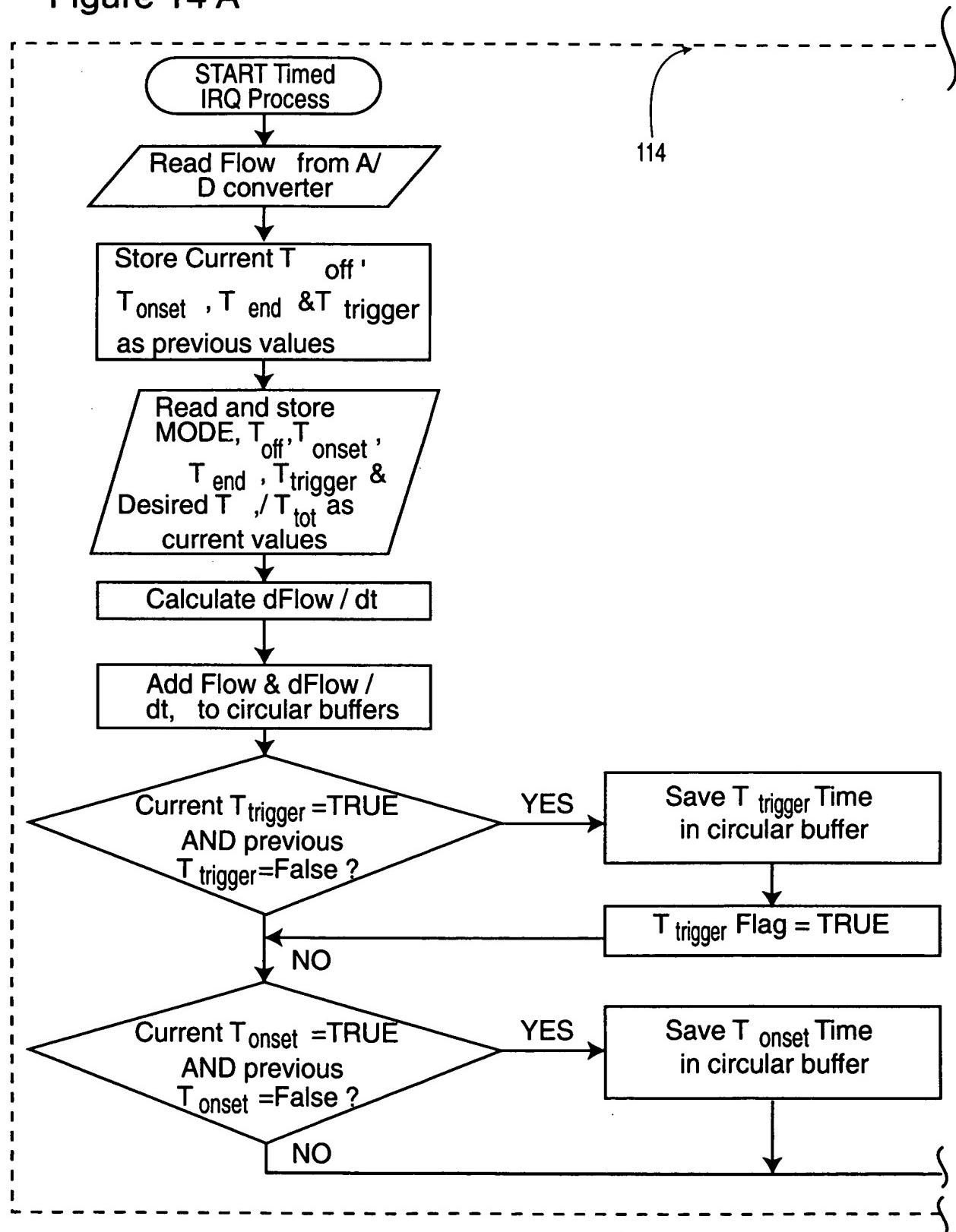




Figure 14 B

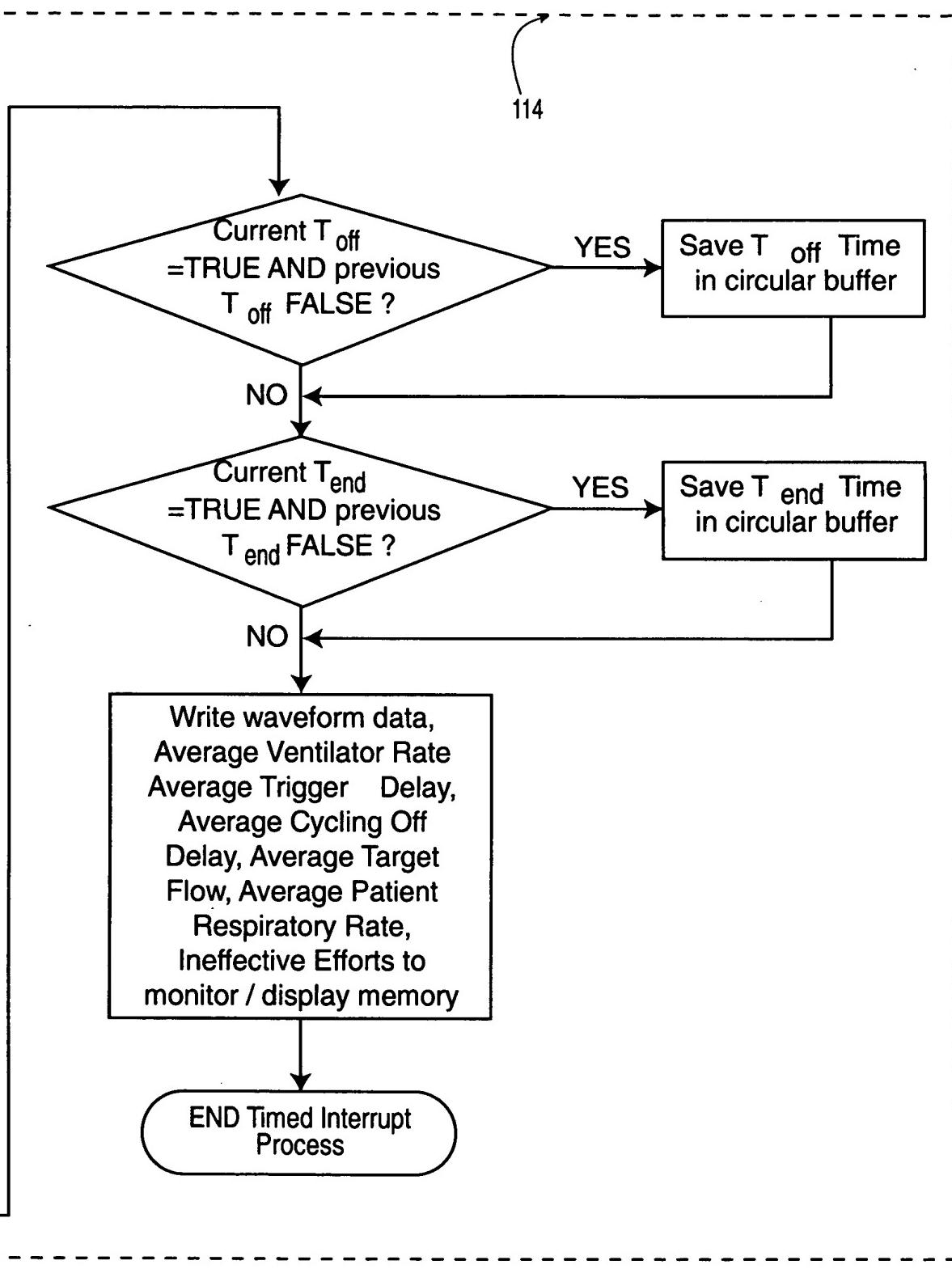




Figure 15 A

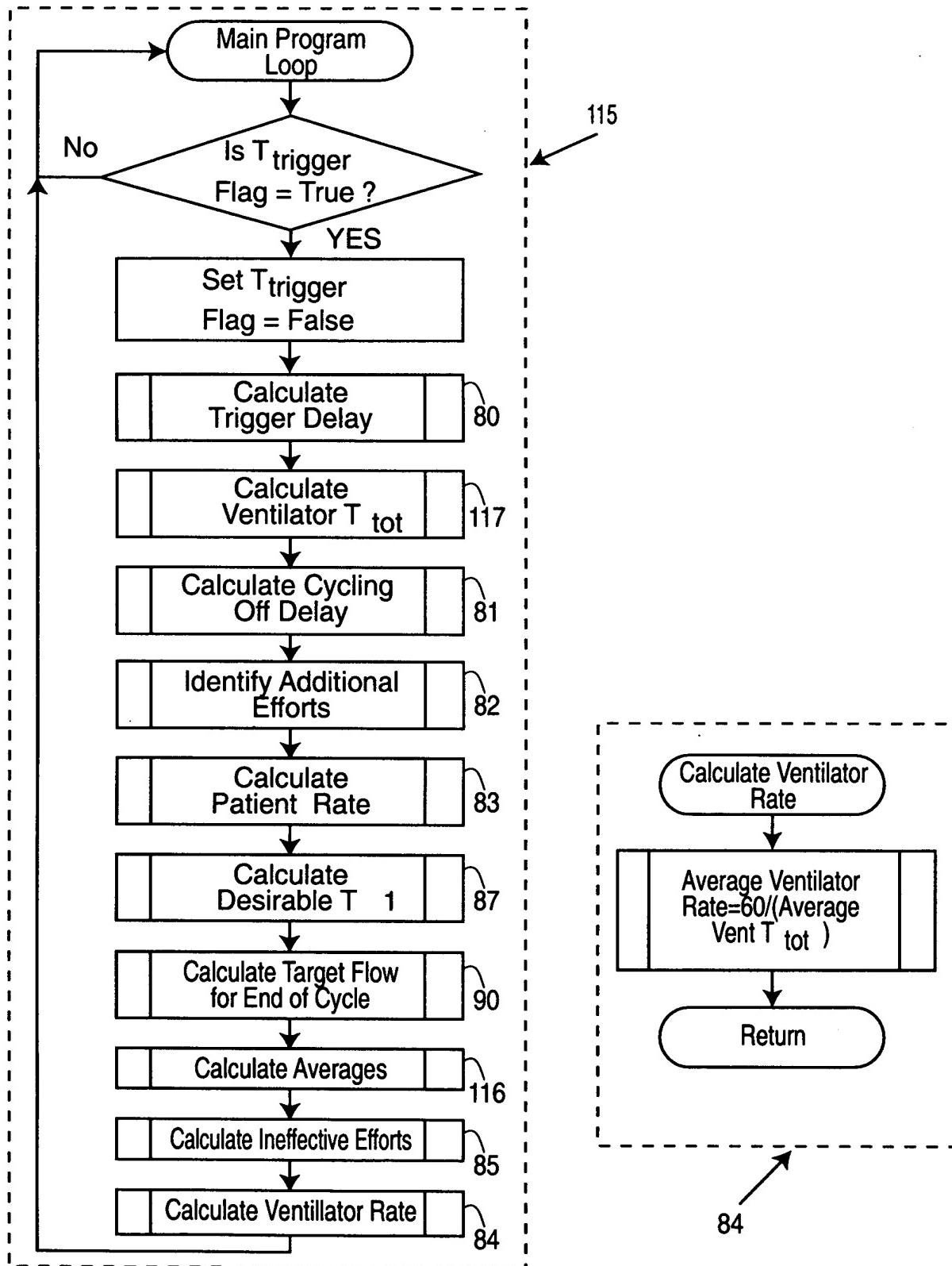




Figure 15 B

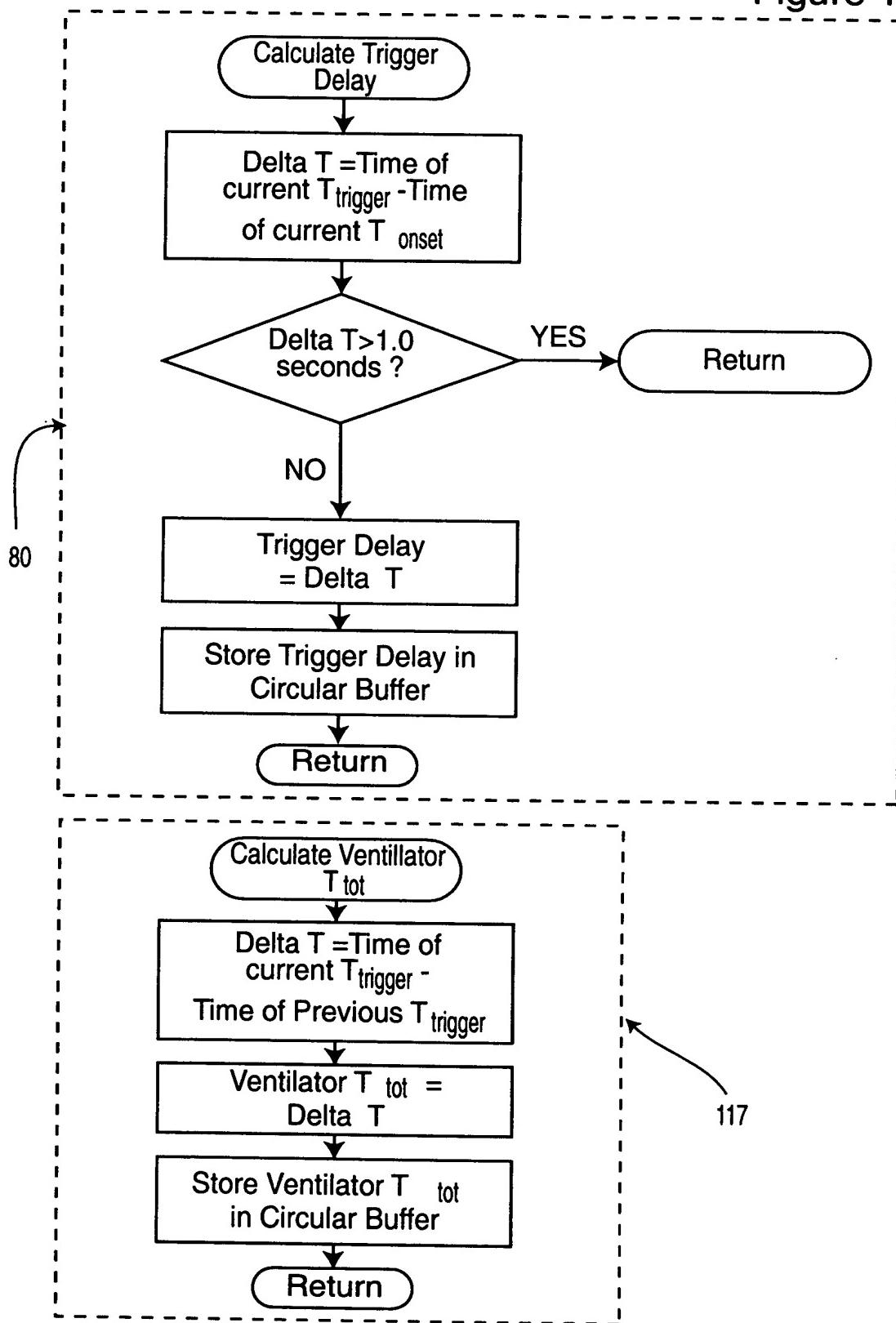




Figure 16 A

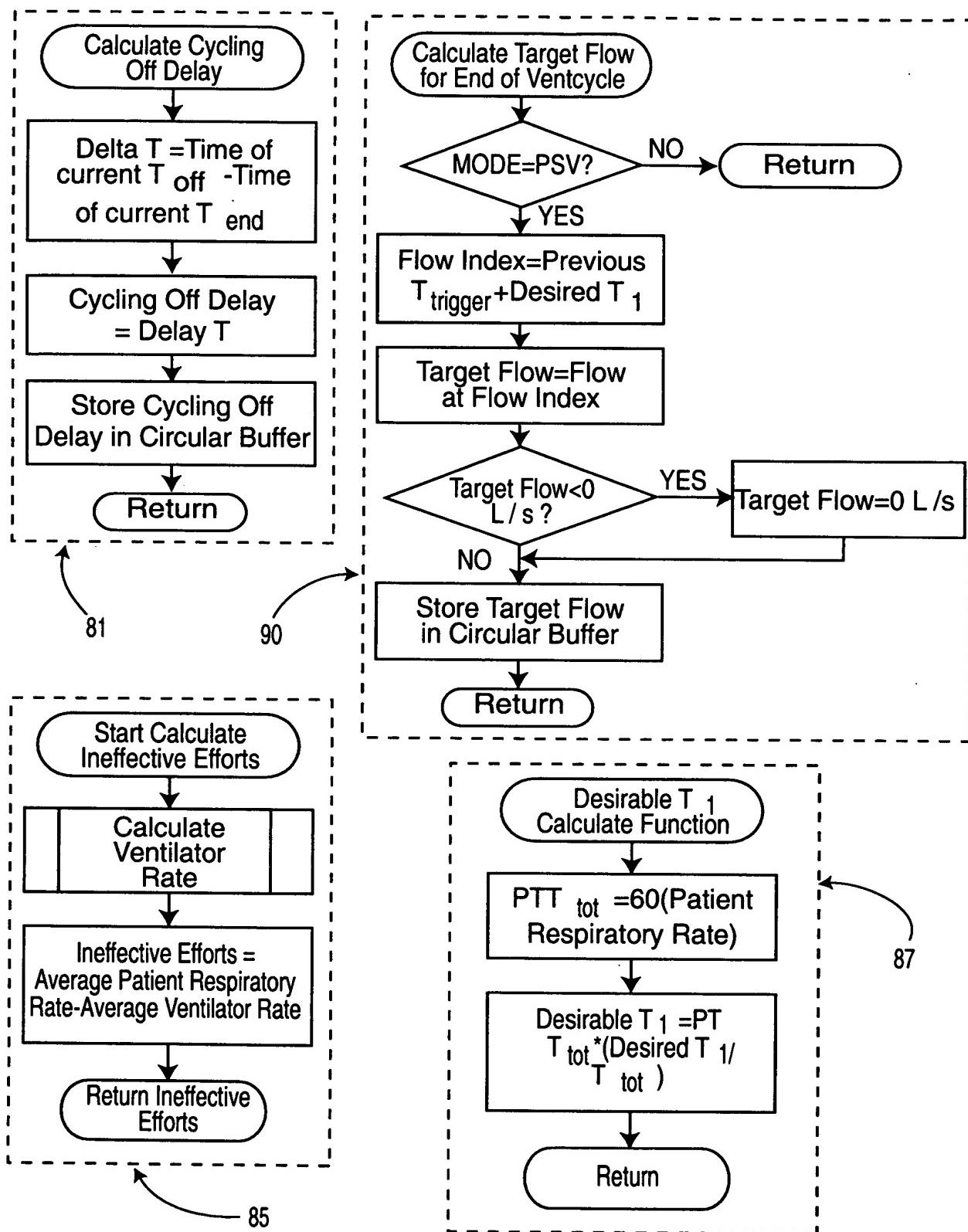




Figure 16 B

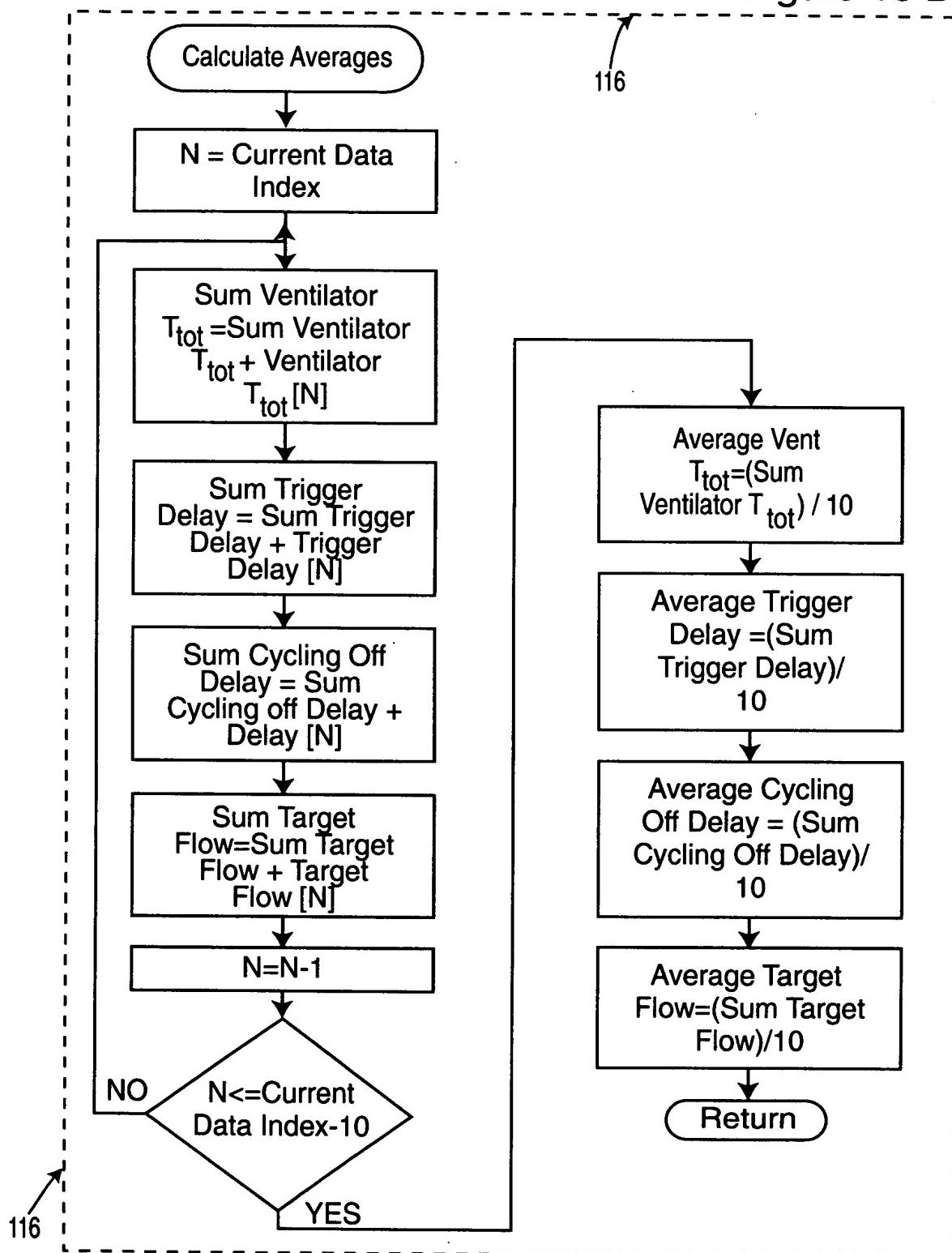
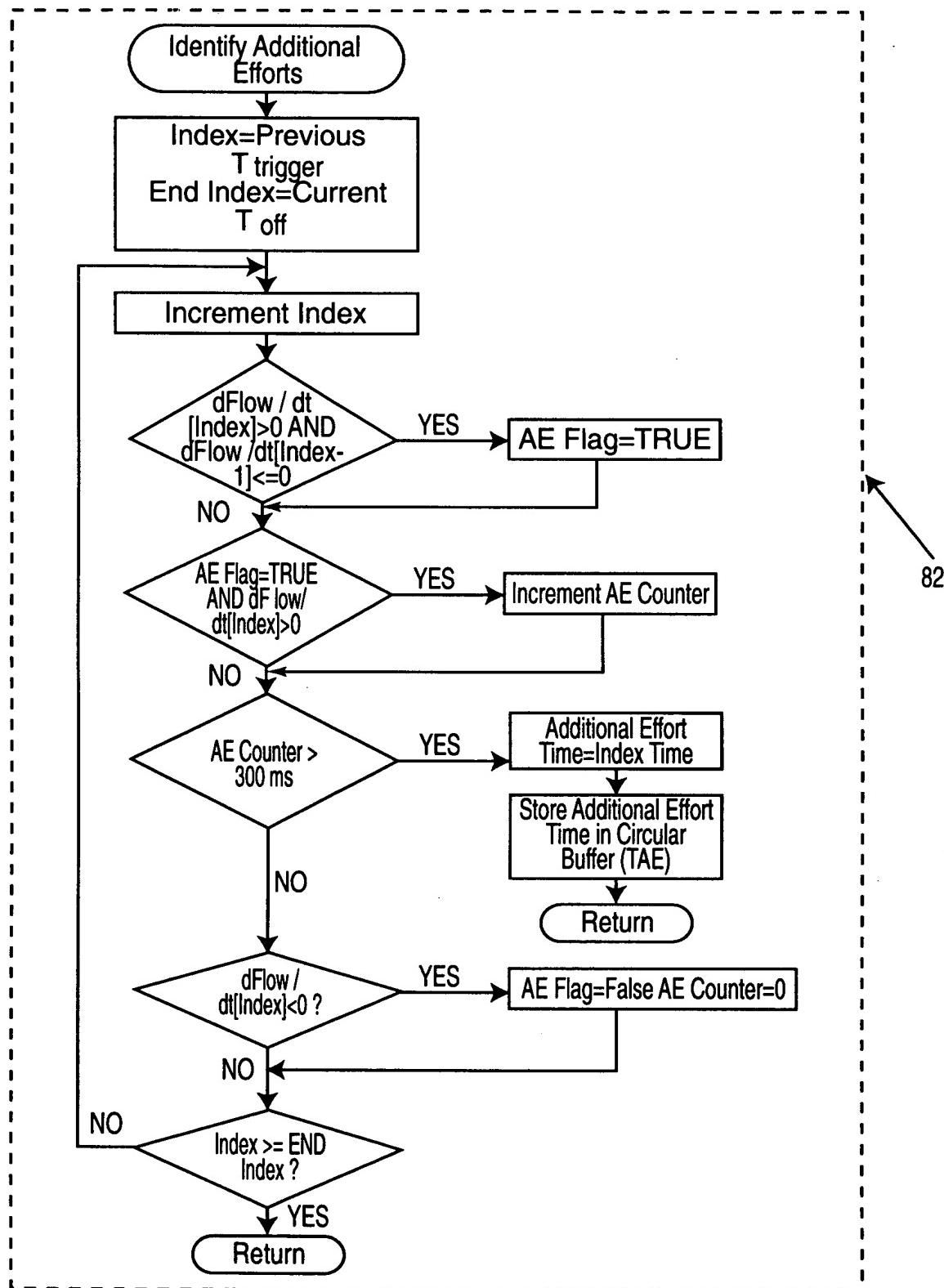




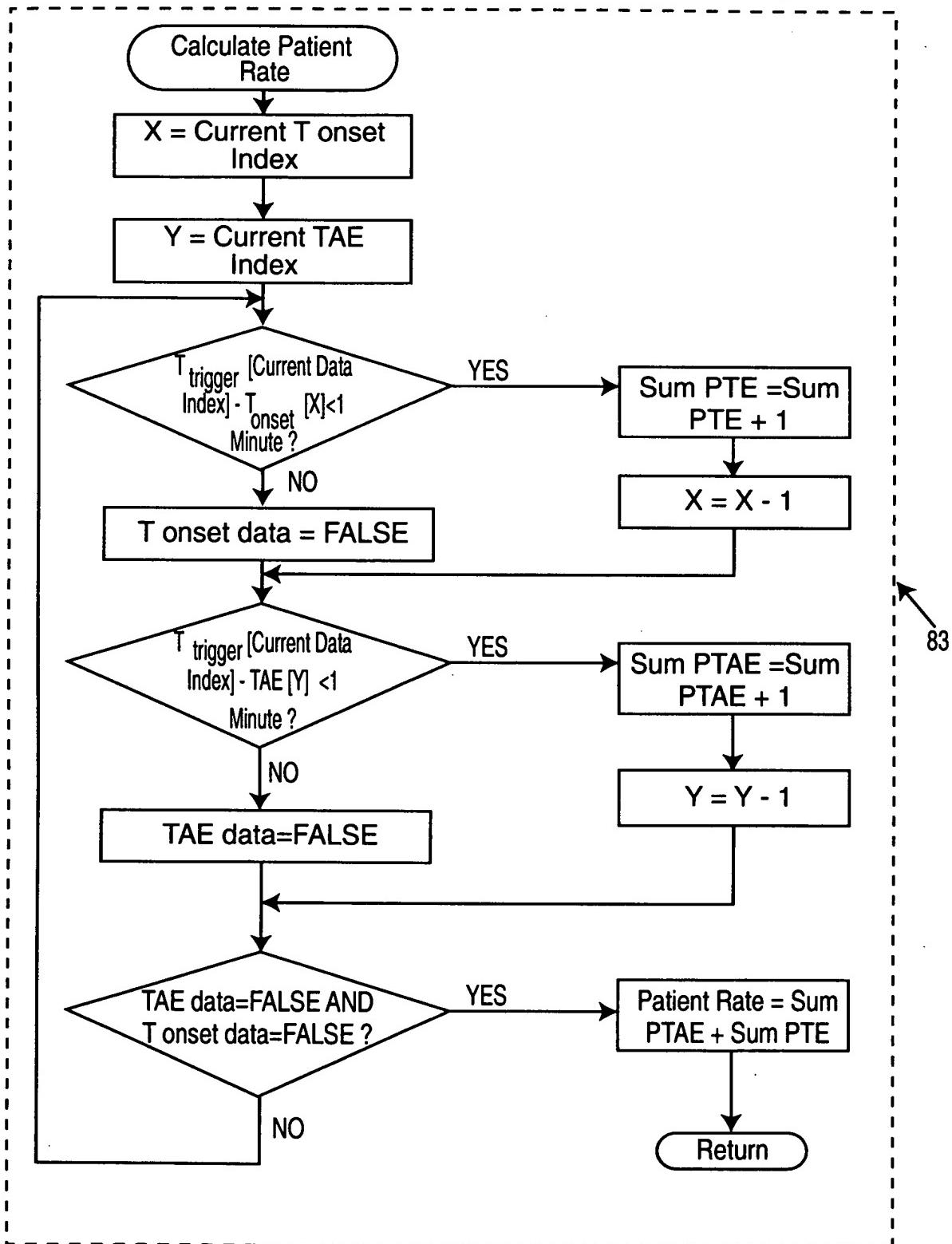
Figure 17 A



82



Figure 17 B



83



Figure 18 A

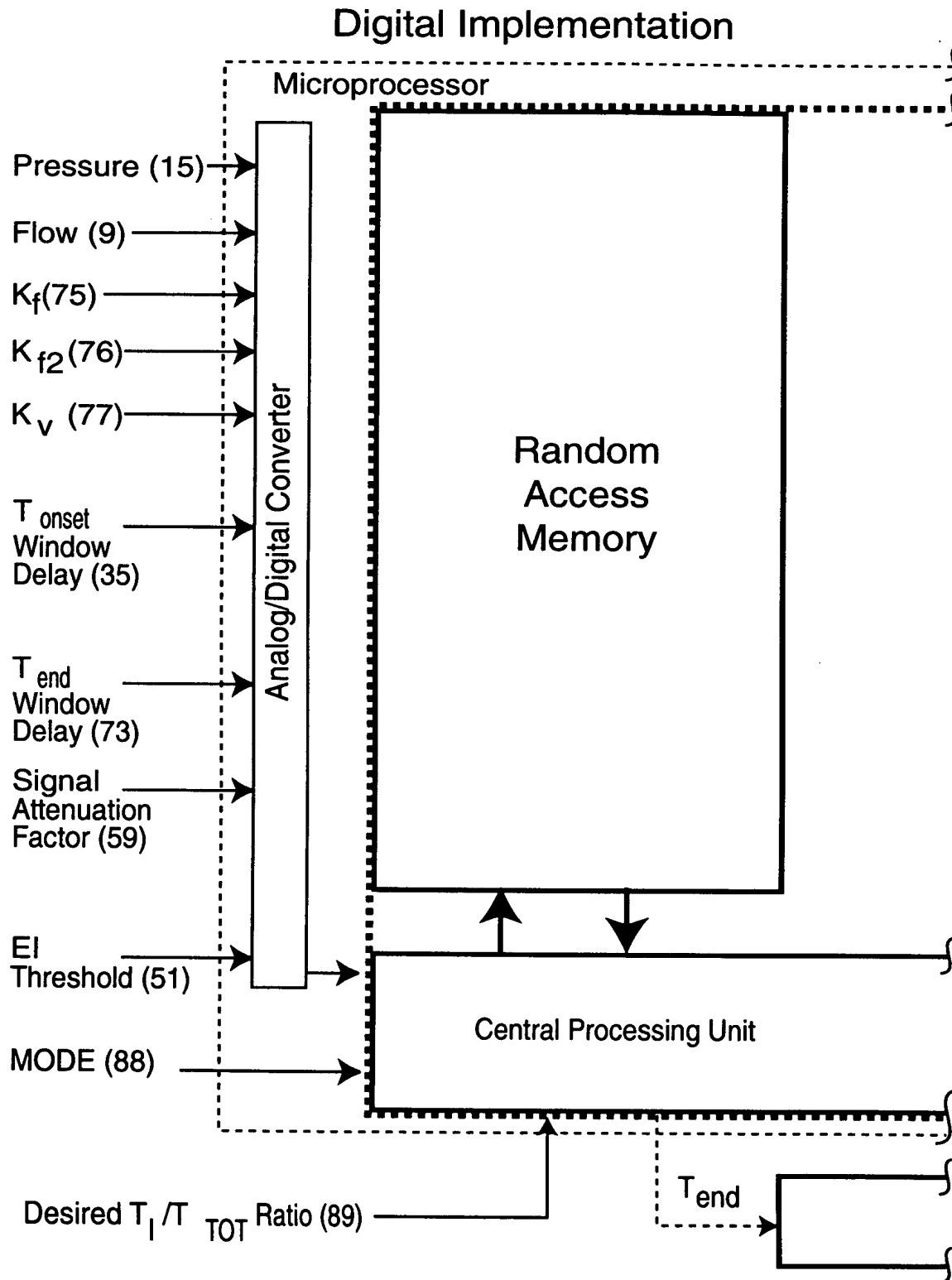




Figure 18 B

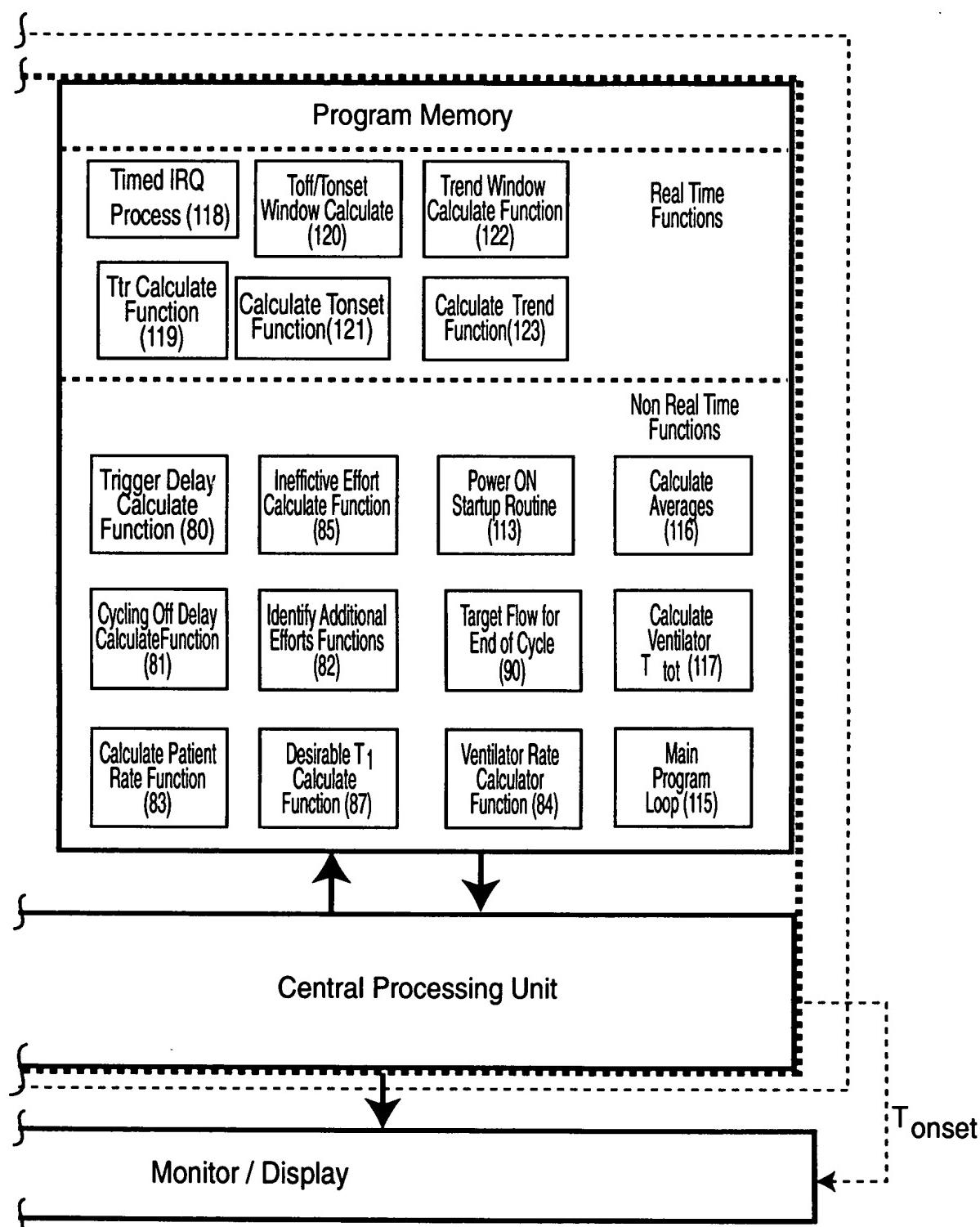




Figure 19 A

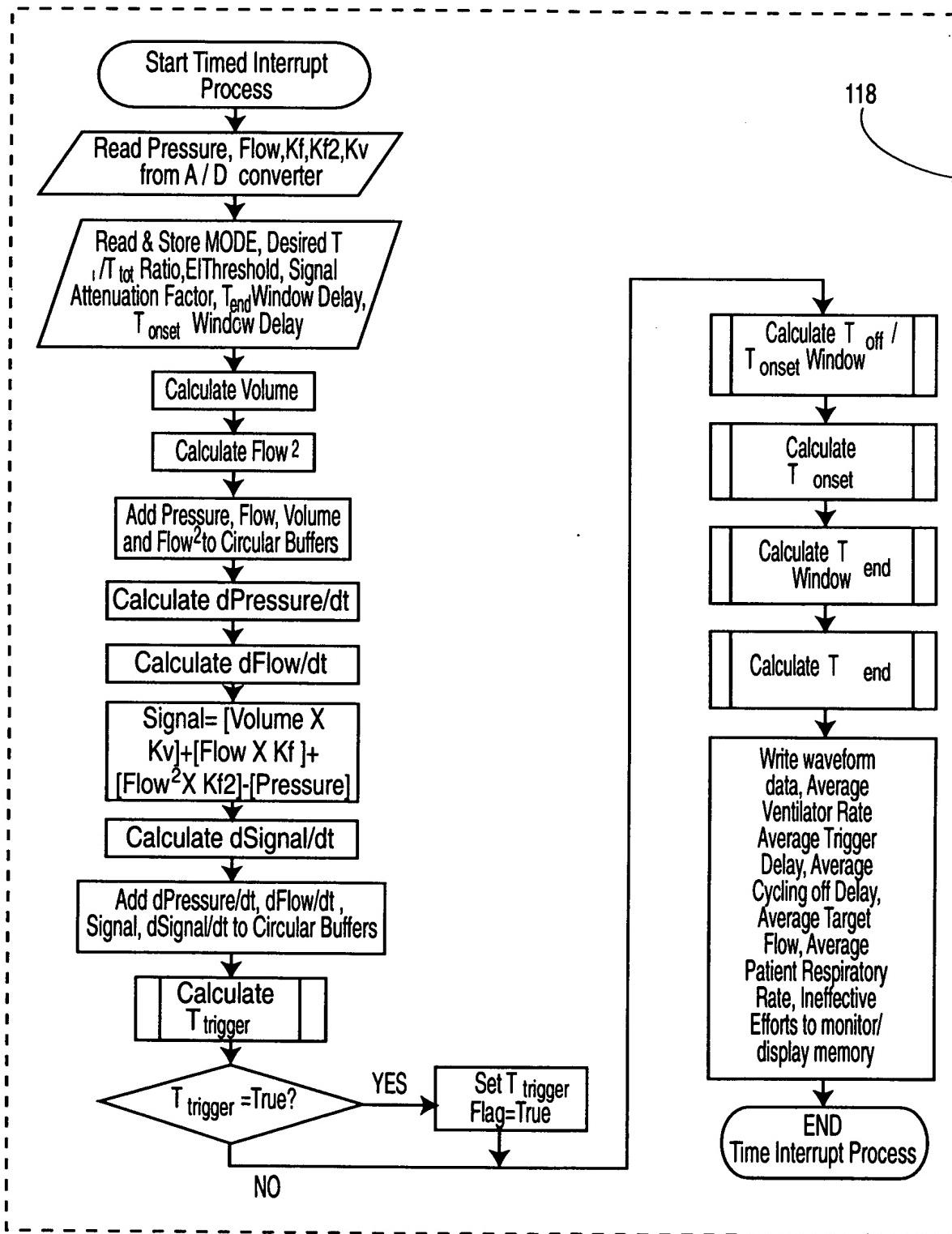




Figure 19 B

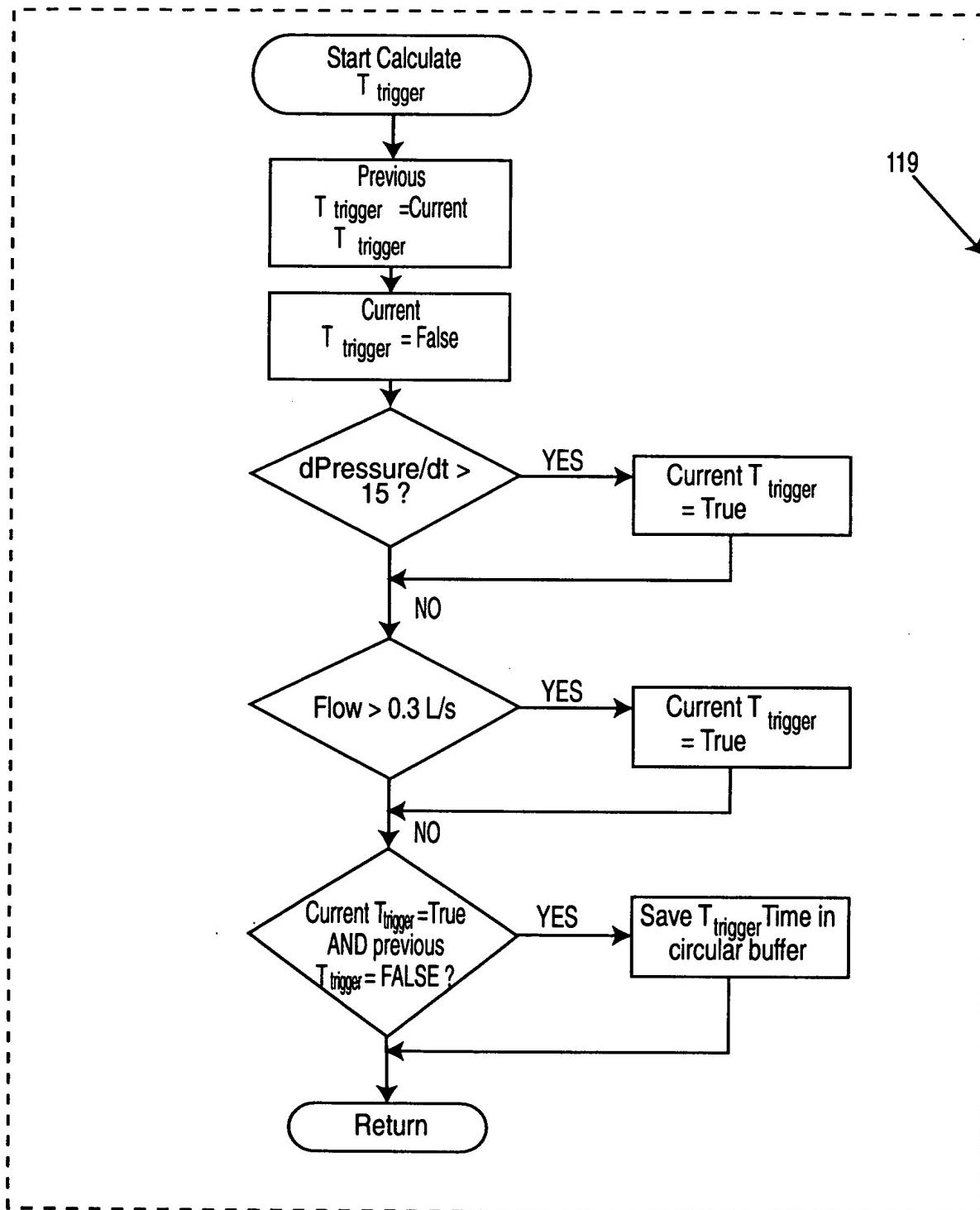




Figure 20 A

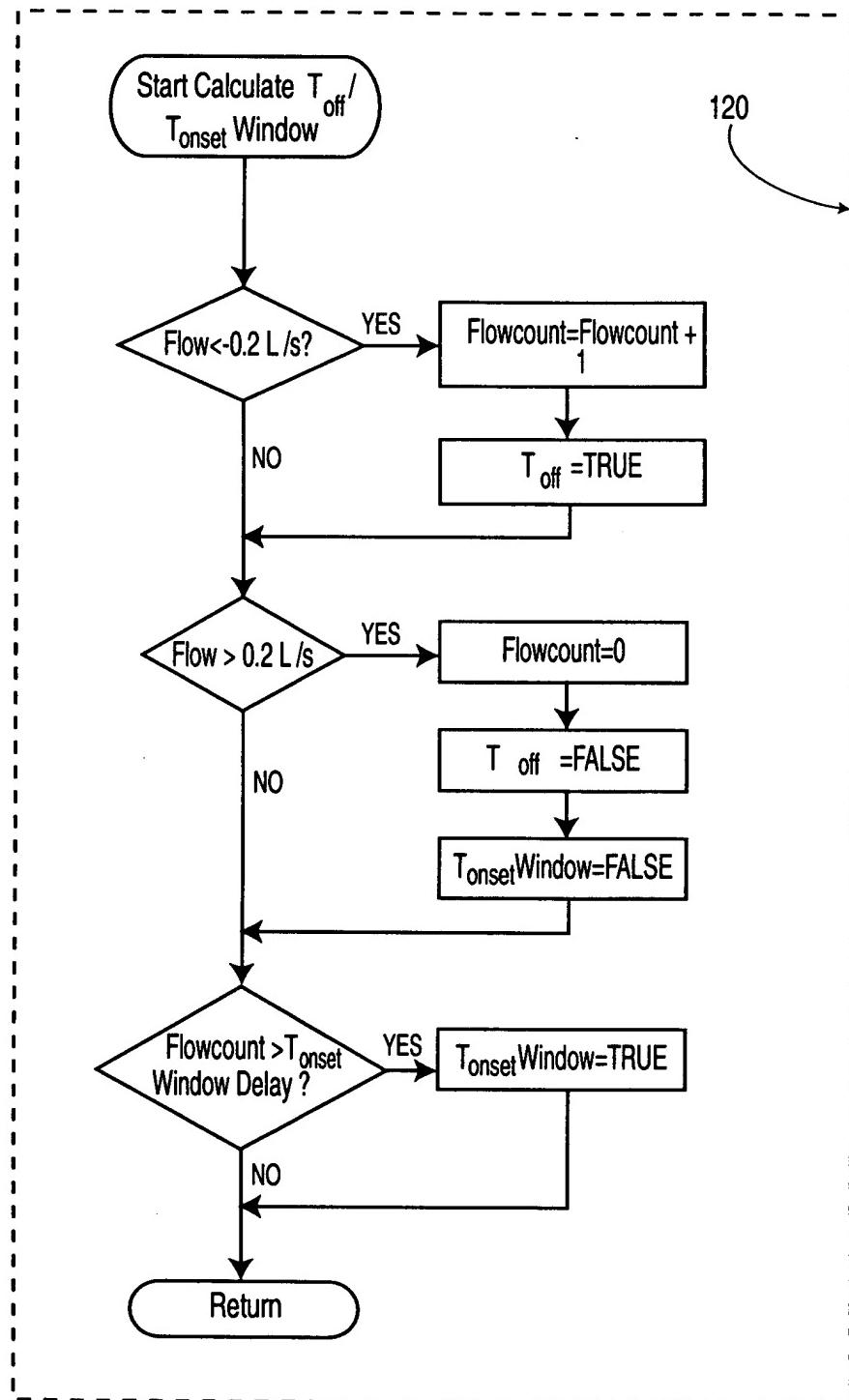




Figure 20 B

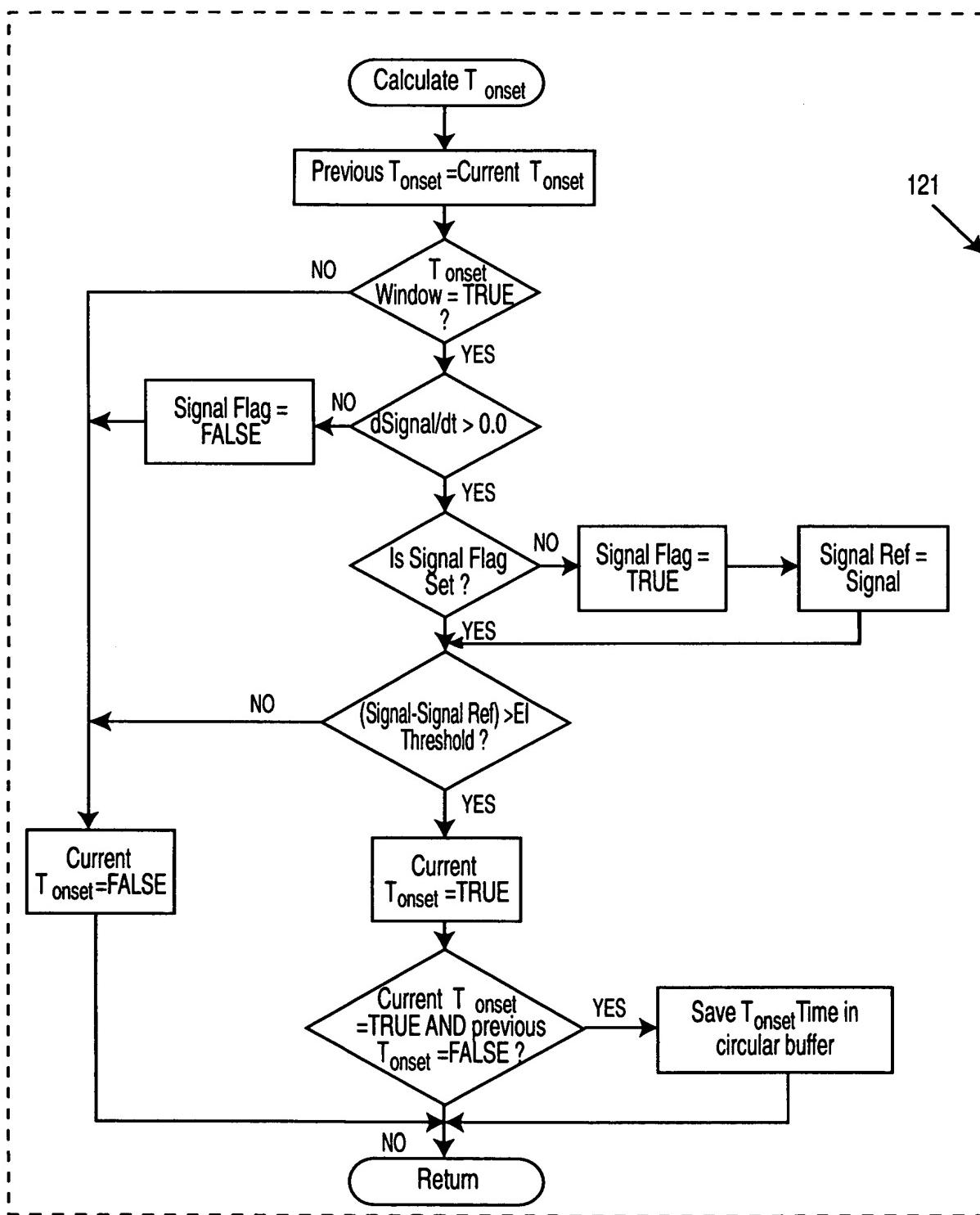




Figure 21 A

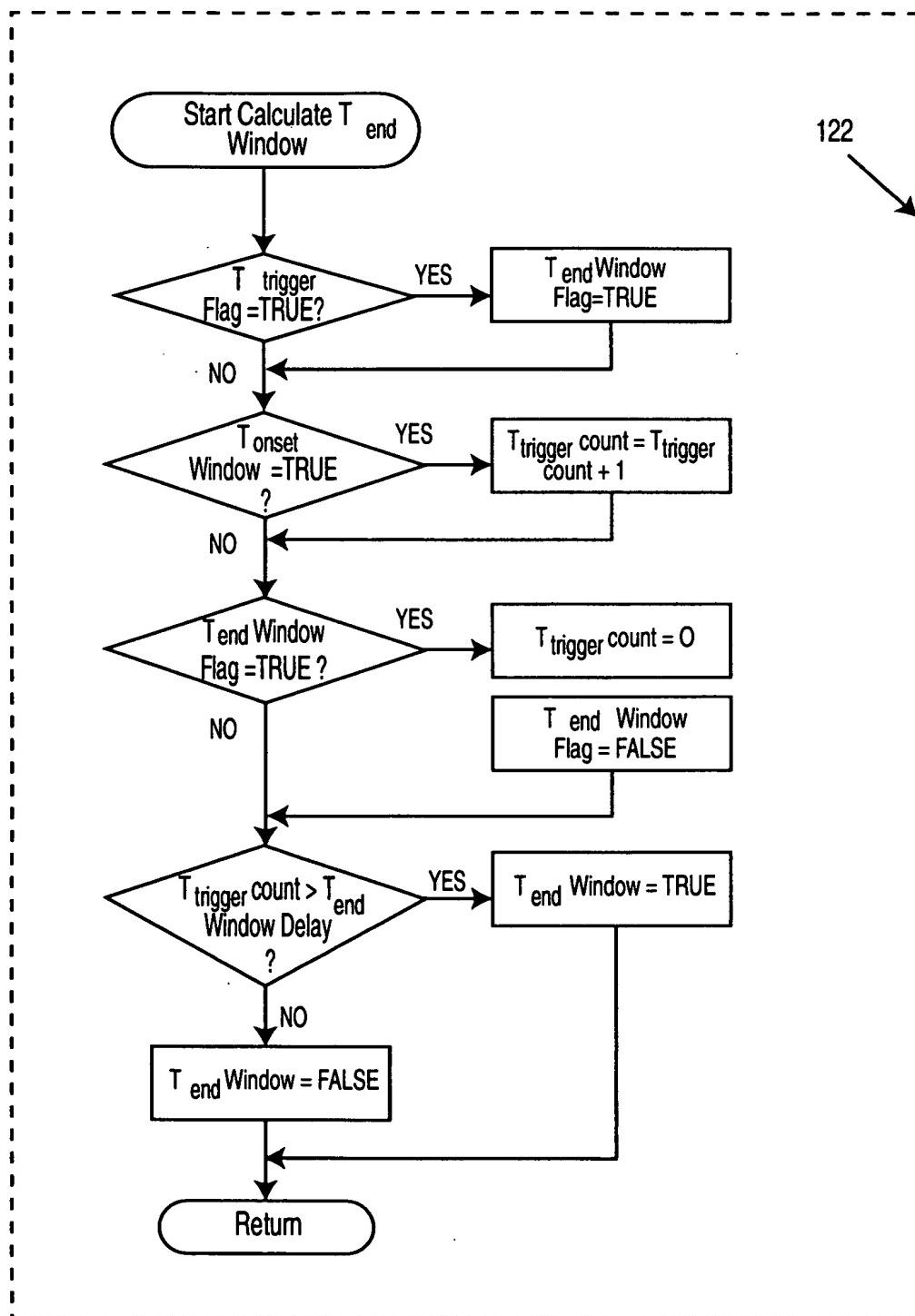




Figure 21 B

